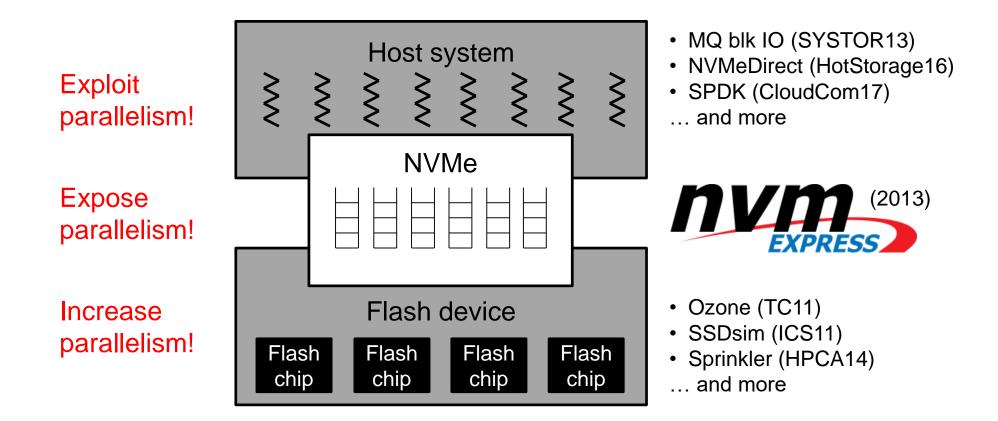
Memory & Storage Architecture Lab. @ Seoul National University

Utilitarian Performance Isolation in Shared SSDs

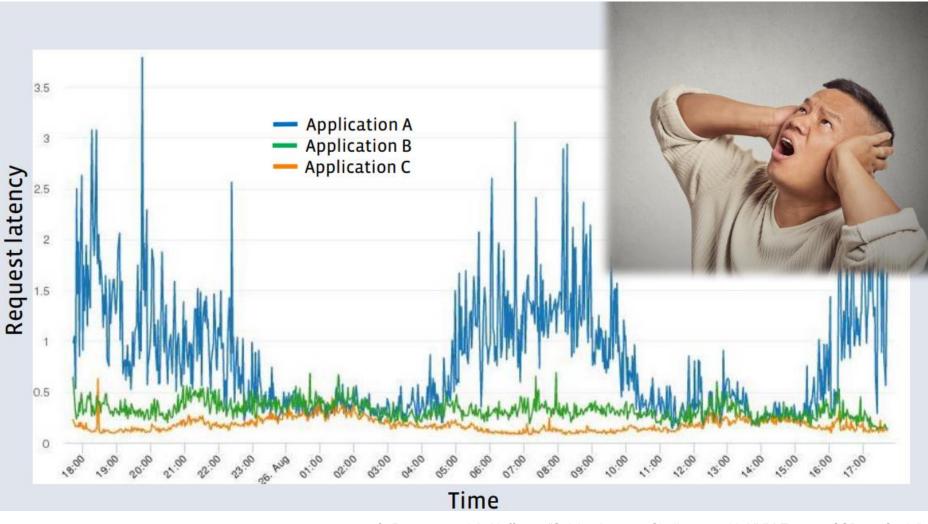
<u>Bryan S. Kim</u> (Seoul National University, Korea)

Flash storage landscape



Memory & Storage Architecture Lab.

Noisy neighbors



C. Petersen and A. Huffman, "Solving Latency Challenges with NVM Express SSDs at Scale", Flash memory summit 2017,

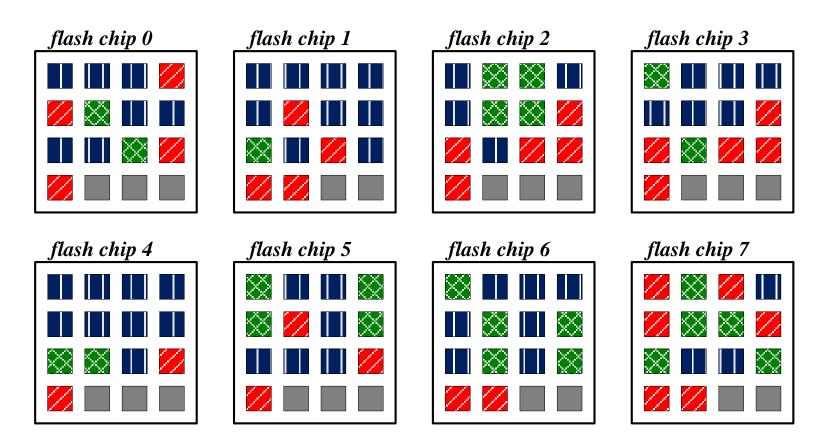
Memory & Storage Architecture Lab.

Background		

Unified sharing of resources (free-for-all)

Blue tenant : large capacity; infrequent access
Red tenant : Write-intensive

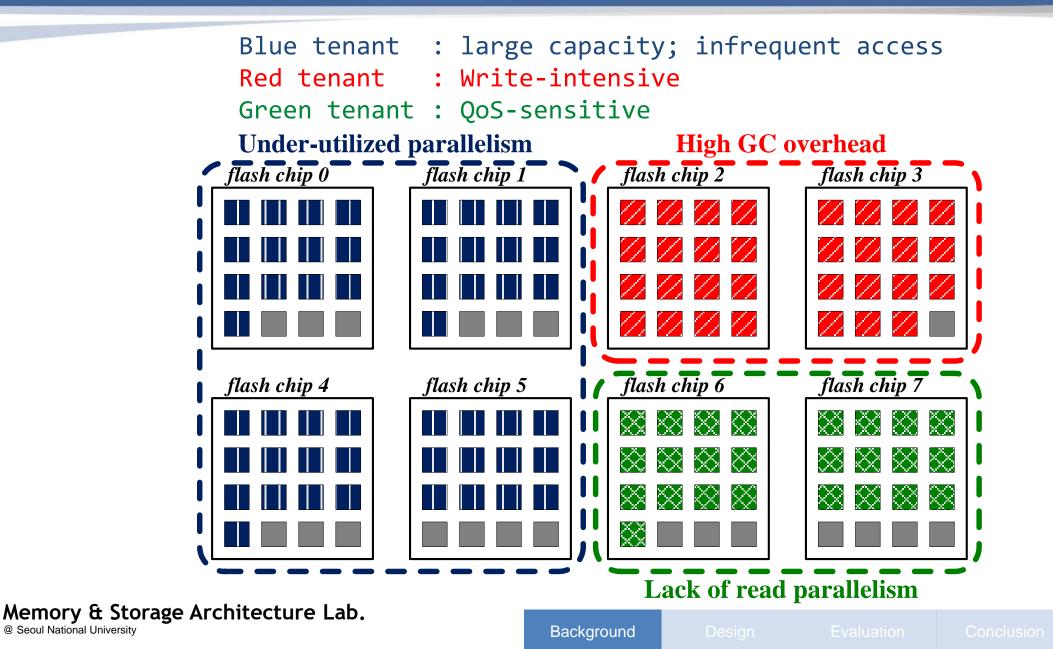
```
Green tenant : QoS-sensitive
```



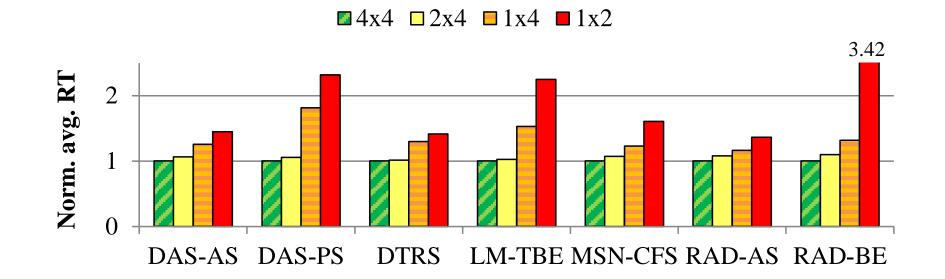
Memory & Storage Architecture Lab. @ Seoul National University

Background

Partitioning of resources (egalitarian)



Slashing parallelism for isolation

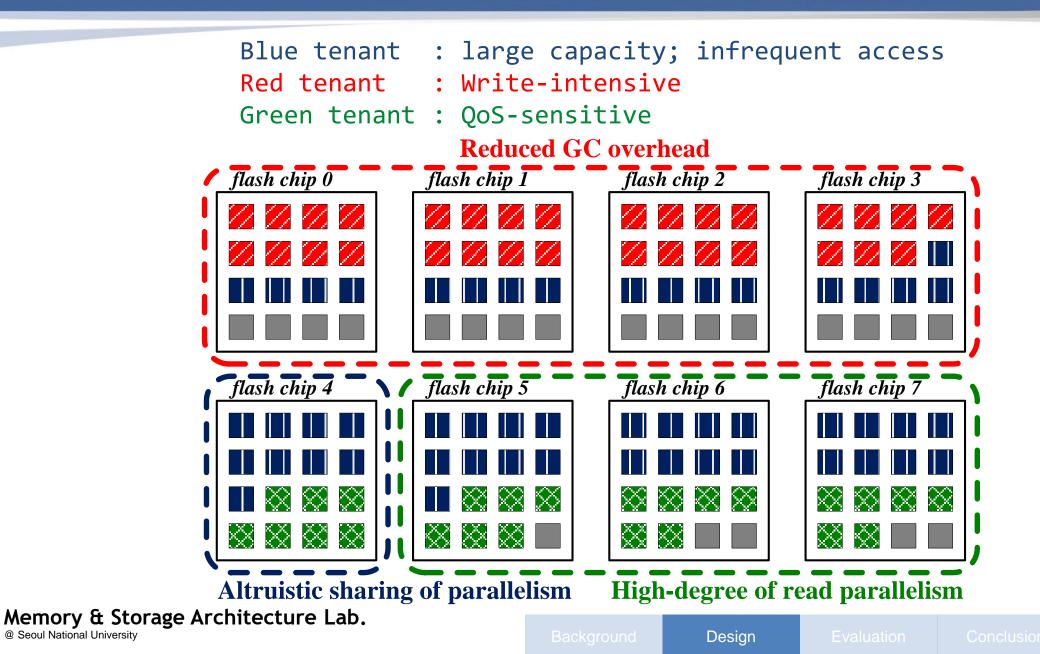


Performance suffers from reduced parallelism!

Background

Memory & Storage Architecture Lab.

Dynamic allocation of resources (utilitarian)



Utilitarian performance isolation

Lessons from storage arrays

- Monitor each tenant's fair share of I/O
- Determine optimal data placement
 - To balance the load across multiple storage devices...
 - ... while considering data relocation overheads

Key insight

- Flash memory's challenges
 - Need to maintain mapping
 - Need to garbage collect

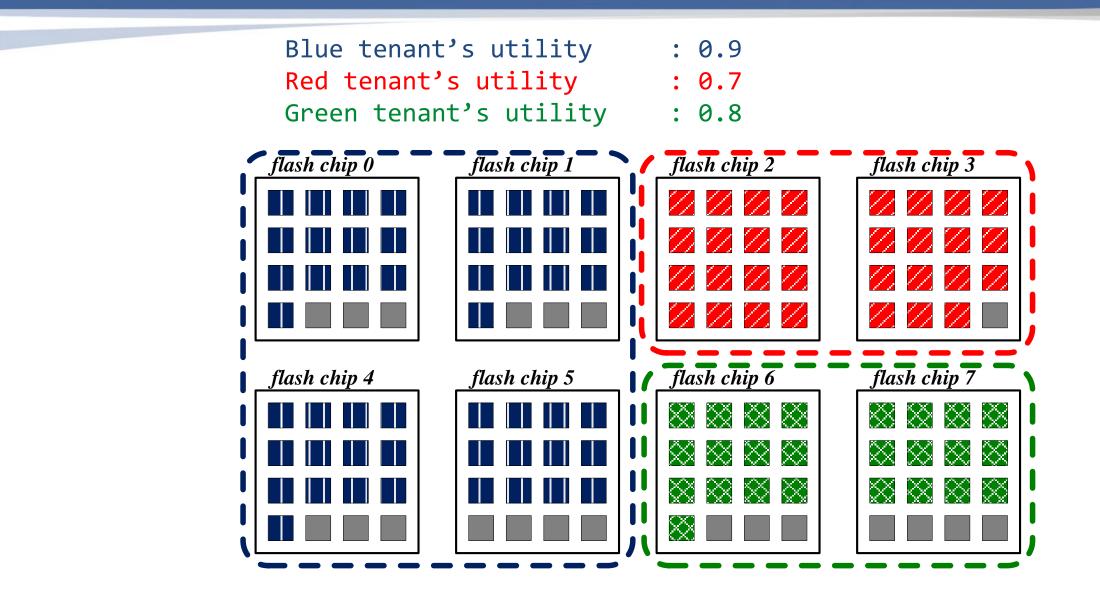
 \rightarrow Flash memory's opportunities

Design

- \rightarrow Easy to balance load
- \rightarrow Easy to relocate data
- The utilitarian approach
 - Compute tenant's utility (measure of received service)
 - Determine the allocation set (a set of chips for writing data) for each tenant
 - Allocation sets are mutually exclusive and collectively exhaustive
 - Allow data relocation among sets if needed

Memory & Storage Architecture Lab. @ Seoul National University

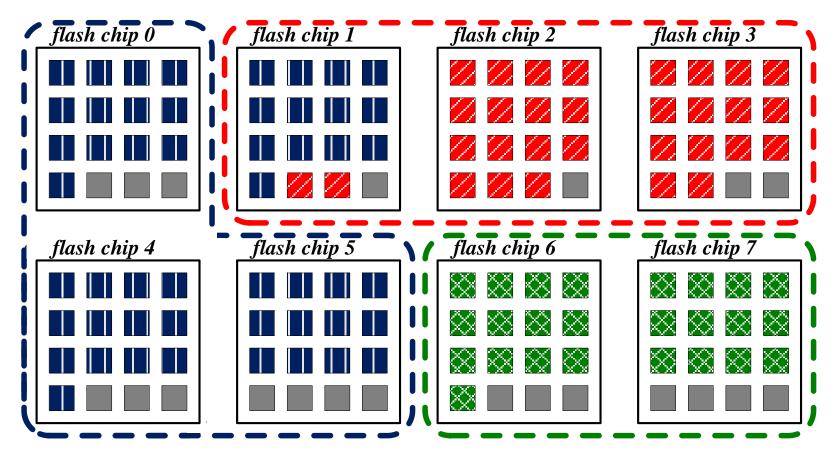
Utility of tenants



Memory & Storage Architecture Lab.

Load balancing

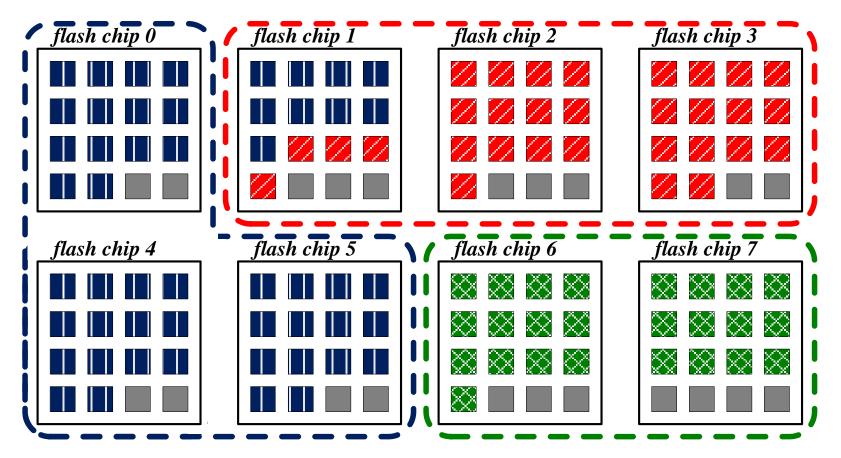
Red tenant's writes are striped across a larger set of flash memory chips. Blue's performance loss is minor.



Memory & Storage Architecture Lab.

Data relocation

Garbage collection in chip 1 isolates some of Blue tenant's data by relocating them to its set. Red tenant's GC efficiency improves.



Memory & Storage Architecture Lab.

Utility

Utility of a tenant is high when its reads experience less traffic

$$Util(t,S) = \frac{\sum_{c}^{chips} N_r(t,c)}{\sum_{c}^{chips} \left(\frac{N_r(t,c)}{1 - Traffic(c,S)}\right)}$$

Traffic

Traffic of a chip indicates the overall busyness of the chip

$$Traffic(c,S) = \frac{\sum_{t}^{tenants} (N_r(t,c) \cdot \tau_r + N_p(t,c,S) \cdot \tau_p)}{Time_{window}}$$

Design

Memory & Storage Architecture Lab.

Set allocation & data relocation

Set allocation

- Objective
 - Find allocation set that minimizes max-min ratio of utility across all tenants
- Approximation
 - Transfer one chip from max utility tenant to min utility tenant
 - Avoid thrashing by transferring only if it balances the utility
 - Select a chip that experienced least number of reads

Data relocation

- Considering the number of reads of a "foreign" block during garbage collection
 - "Foreign" blocks that high number of reads are incentivized to relocate to its own set
 - Infrequently accessed cold data may remain in another set

Memory & Storage Architecture Lab.

Evaluation environment & methodology

Storage system configuration

- 150GB storage with 28% over-provisioning
 - 3 channels x 4 chips/chan
- Garbage collection: reclaims space for writes + considers "foreign" block reads

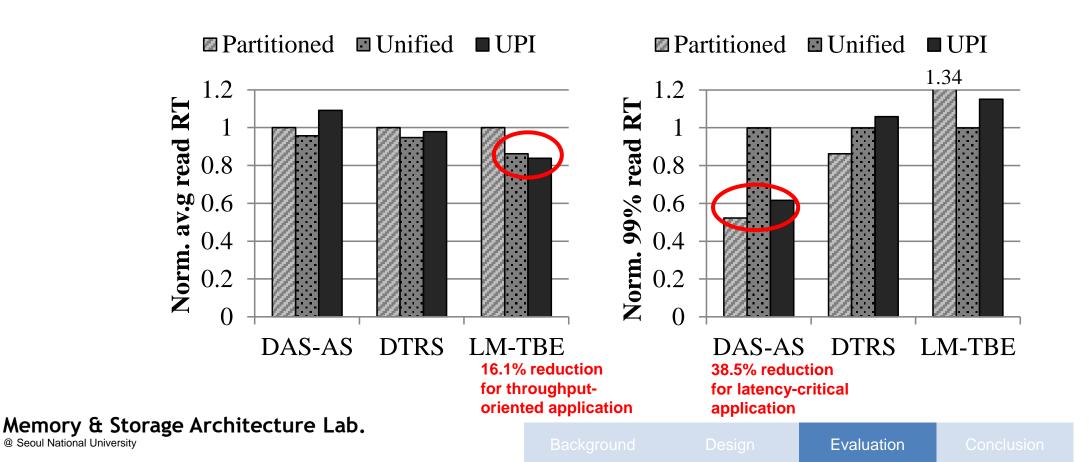
Workload configuration

- 3 real-world I/O traces collected from MS production servers
 - DAS-AS: lowest throughput, highest read-to-write ratio
 - DTRS: relatively random workload with bursts of writes
 - LM-TBE: large sequential reads and writes

Memory & Storage Architecture Lab.

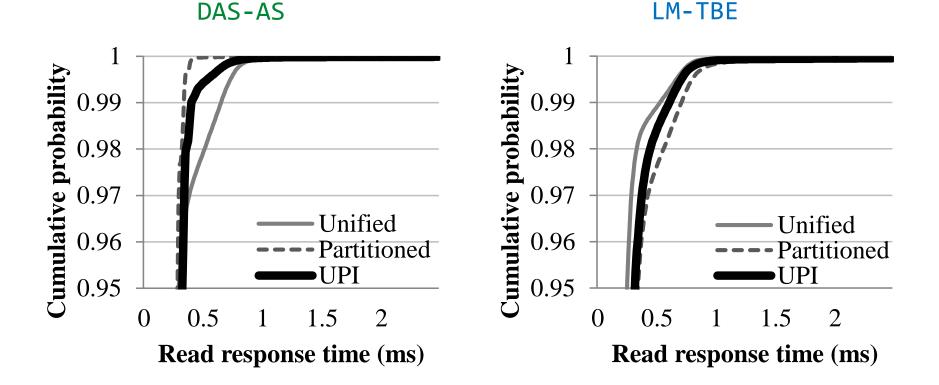
Average performance

Partitioned: dedicates channel to each tenantUnified: shares all resources among tenantsUPI: dynamically allocates based on utility



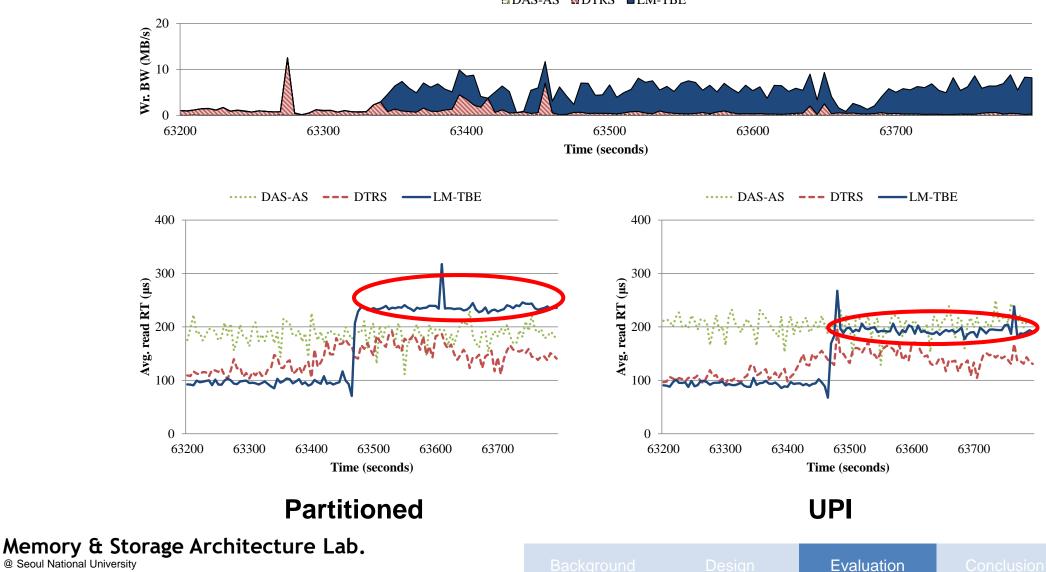
QoS performance

Partitioned: dedicates channel to each tenantUnified: shares all resources among tenantsUPI: dynamically allocates based on utility



Memory & Storage Architecture Lab.

Microscopic view



Conclusion

Dynamic allocation of resources based on utility

- Decouple parallelism, isolation, and capacity
- Balancing the load by distributing write traffic
- Relocate data through existing SSD management mechanisms

Utilitarian Performance Isolation

reduces average response time by 16.1% for high-throughput workload reduces 99% QoS by 38.5% for latency-critical workload

Memory & Storage Architecture Lab.

@ Seoul National University

kground Design

Evalu

Conclusion