

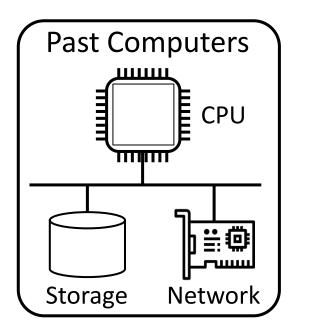


Identifying On-/Off-CPU Bottlenecks Together with Blocked Samples

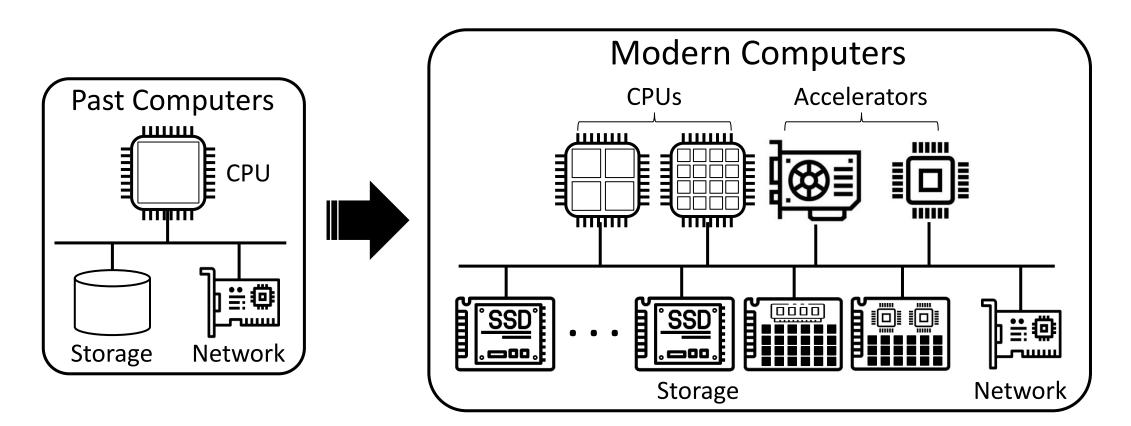
Minwoo Ahn¹, Jeongmin Han¹, Youngjin Kwon² and Jinkyu Jeong³

- ¹ Sungkyunkwan University
- ² Korea Advanced Institute of Science and Technology (KAIST)
- ³ Yonsei University

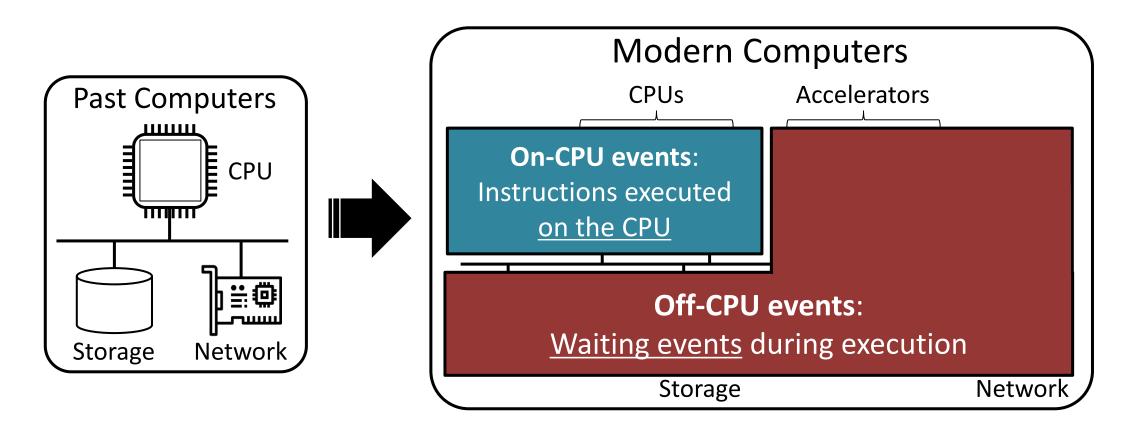
- Computing environments are becoming more complex and advanced
 - Events executed outside the CPU (i.e., <u>off-CPU</u>) have become more diverse



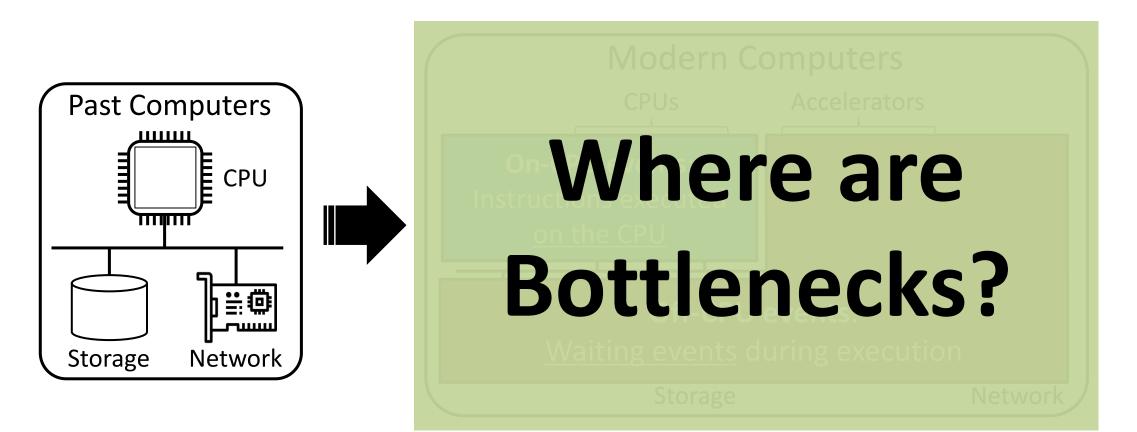
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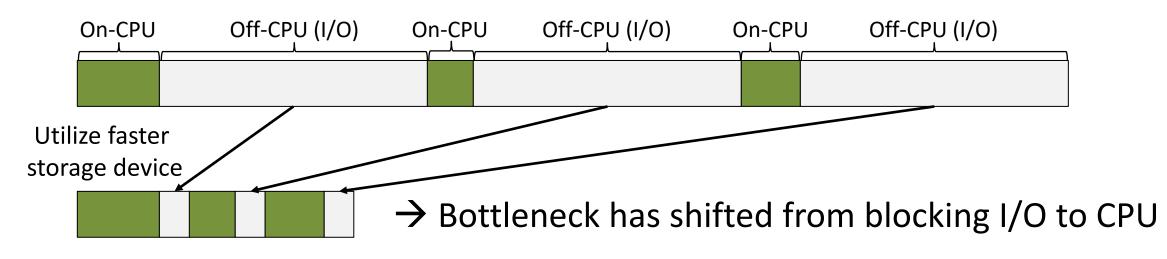


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 - Events executed outside the CPU (i.e., <u>off-CPU</u>) have become more diverse



Bottlenecks of Modern Applications

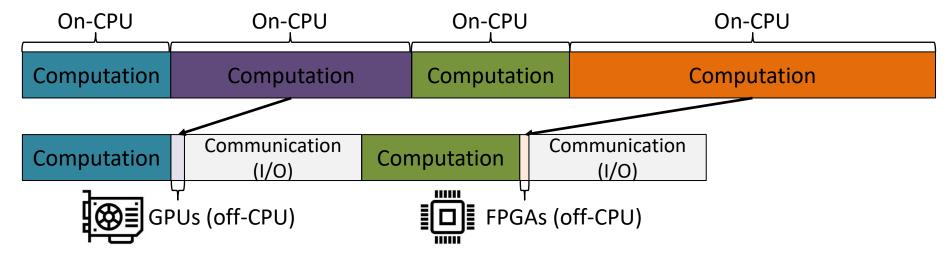
- Bottlenecks of applications are diversifying
 - (I/O) Boundary between CPU-bound and I/O-bound is blurred



- "kernel software is becoming the bottleneck", XRP [OSDI '22]
- "server CPU is becoming the bottleneck", XSTORE [OSDI '20]
- "Rocksdb is CPU-bound", Kvell [SOSP '19]
- "kernel I/O stack accounts for a large fraction", AIOS [ATC '19]
- "storage no longer being the bottleneck", uDepot [FAST '19]

Bottlenecks of Modern Applications

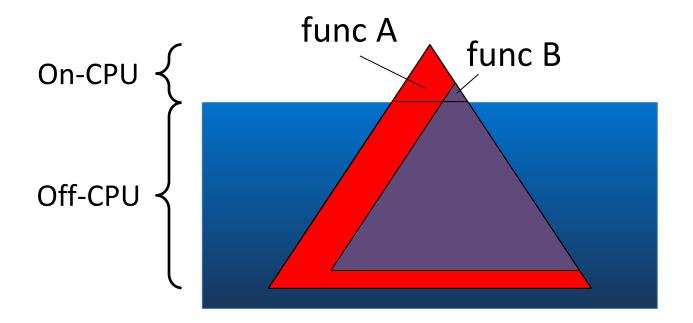
- Bottlenecks of applications are diversifying
 - (I/O) Boundary between CPU-bound and I/O-bound is blurred
 - (<u>Computation</u>) Shifting away from CPU-centric computations



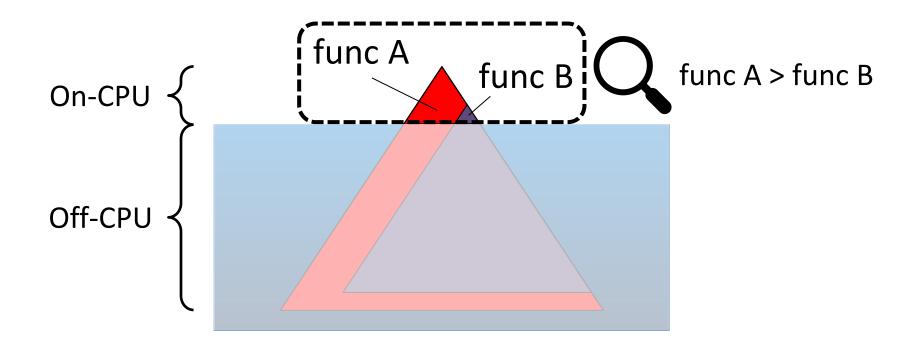
 \rightarrow Bottleneck has shifted from CPU computation to I/O and communication

- "there are spare CPU and network bandwidth", BytePS [OSDI '20]
- "rapid increases in GPU will shift the bottleneck towards communication", PipeDream [SOSP '19]
- "DNN training is not scalable, mainly due to the communication overhead", ByteScheduler [SOSP '19]

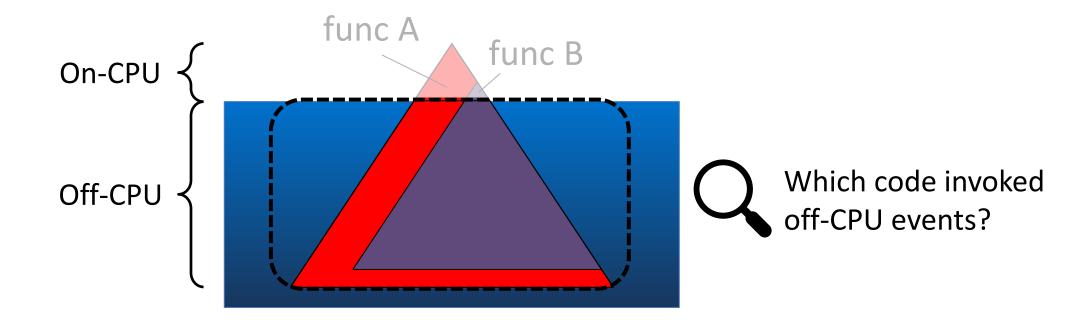
- Both on-CPU and off-CPU events need to be considered <u>simultaneously</u>
 - (Challenge #1) Analysis is conducted using only partial information



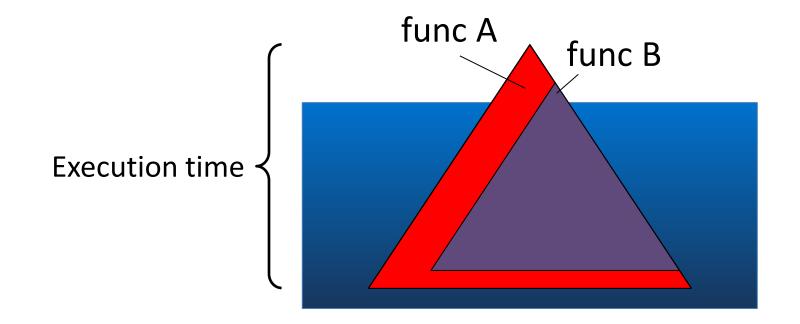
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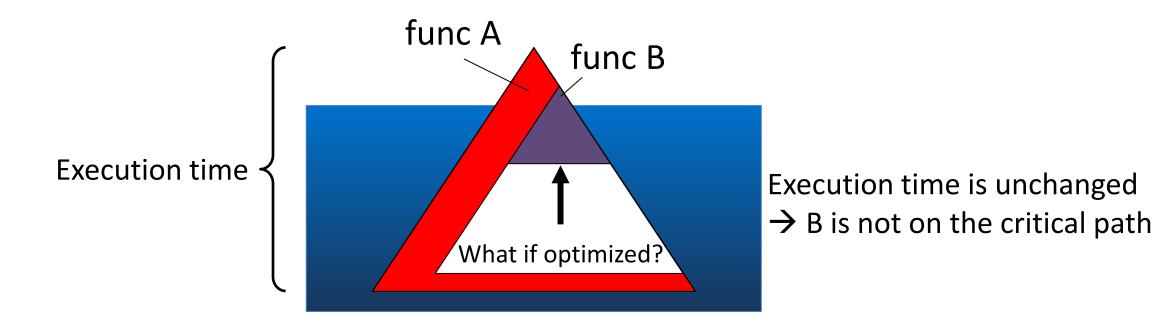
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 - (Challenge #2) Hard to assess the <u>impact of optimizing</u> off-CPU events

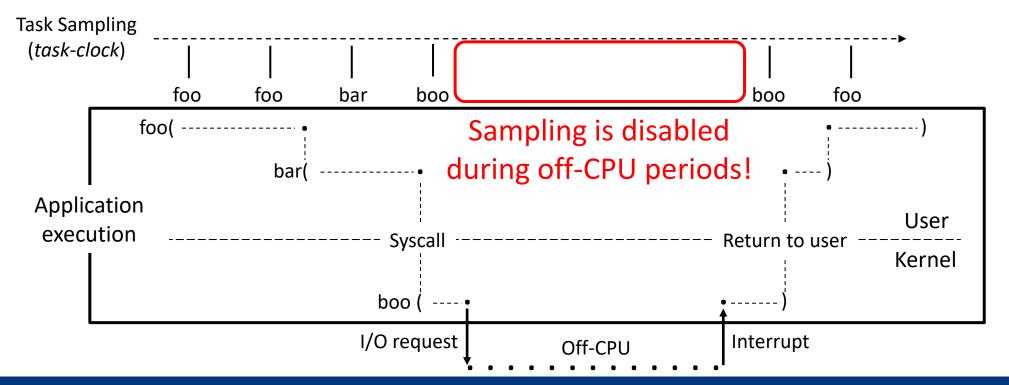


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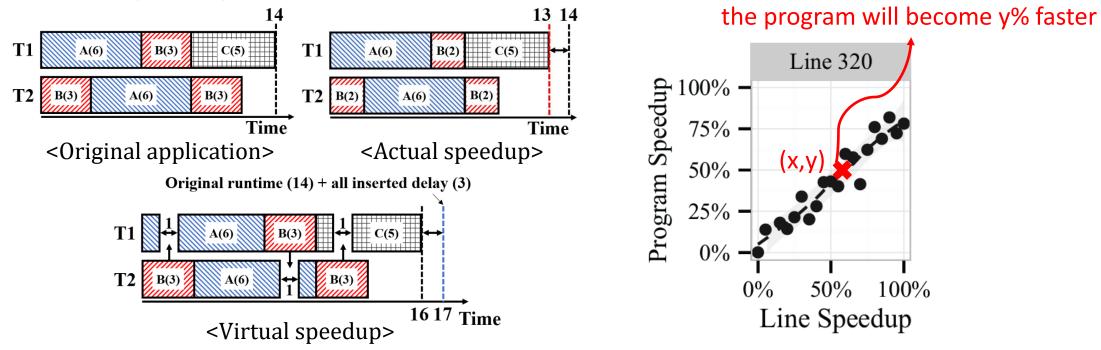
On-CPU Analysis

- Linux perf sampling (task-clock)
 - Feature in Linux kernel's perf subsystem
 - Collects profiling information (e.g., IP and callchain) periodically
 - A Low overhead, effective technique to analyze on-CPU behavior



On-CPU Analysis

- COZ [SOSP '15]
 - Predict the impact of optimizing the specific code line without actual optimization
 - Virtual speedup

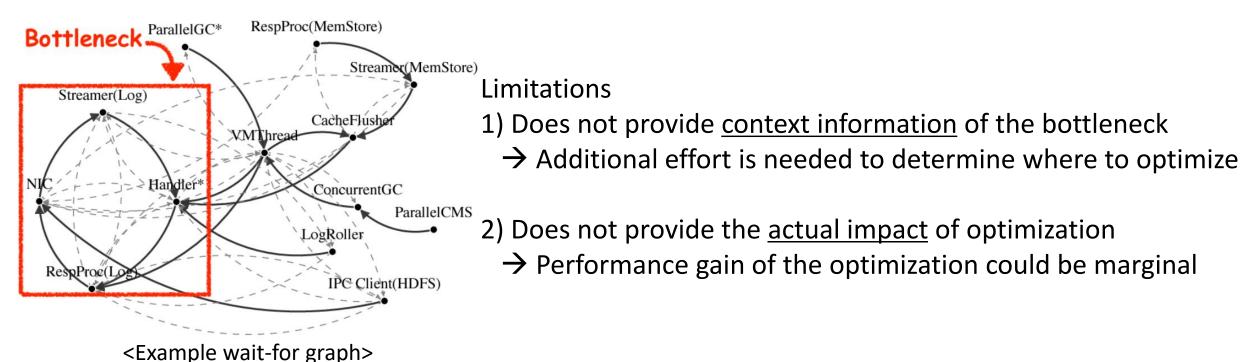


COZ utilizes on-CPU sampling (Linux *perf*) \rightarrow Virtual speedup is <u>limited to only on-CPU events</u>

If line 320 becomes x% faster,

Off-CPU Analysis

- wPerf [OSDI '18]
 - Traces all kinds of waiting events including I/O and their dependencies
 - Wait-for graph: Dependency graph of executed threads
 - Identifying closed loops (i.e., knots) through graph analysis



Summary of the Limitations

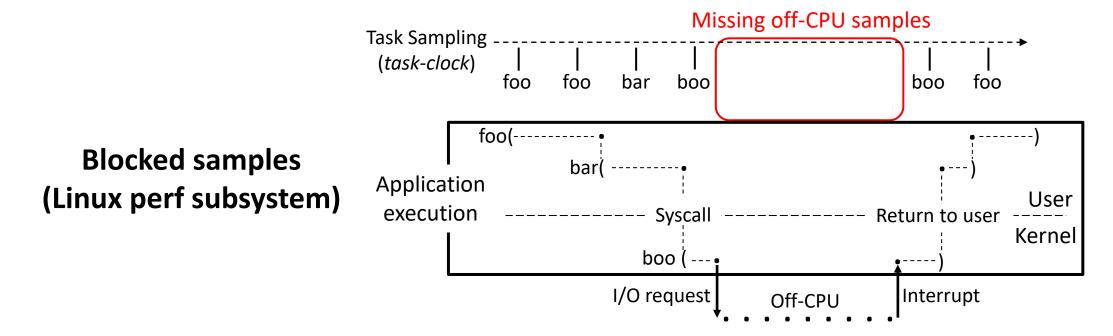
 \rightarrow (Limitation #1) Focuses solely on either on-CPU or off-CPU events

→ (Limitation #2) Causality analysis is not supported for off-CPU events

Profiler	Profiling Scope	Causality Analysis	
Linux perf		X	
COZ	COZ On-CPU		
wPerf	Off-CPU	X	
Blocked Samples	Both on-/off-CPU	0	

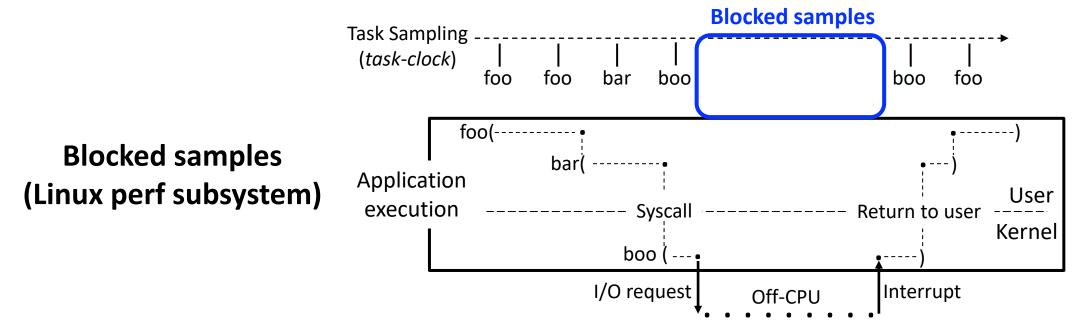
Our Approach: Blocked Samples

• Goal: sampling on- and off-CPU events simultaneously



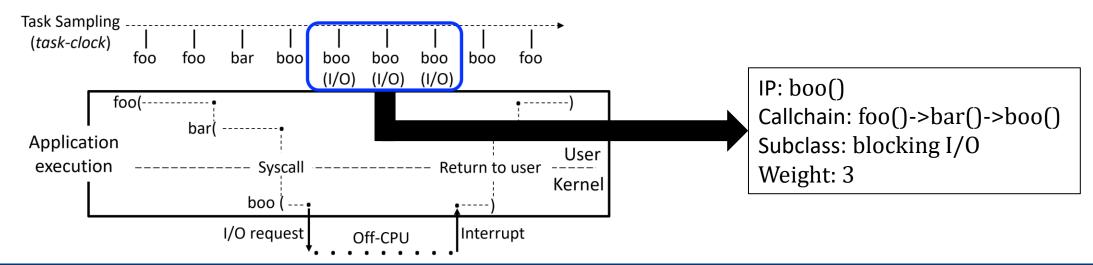
Our Approach: Blocked Samples

- Goal: sampling on- and off-CPU events simultaneously
 - Blocked samples: sampling technique for off-CPU events
 - Proposed profilers using blocked samples
 - bperf: sampling-based statistical profiler on both on-/off-CPU events
 - BCOZ: causal profiler that supports virtual speedup on both on-/off-CPU events



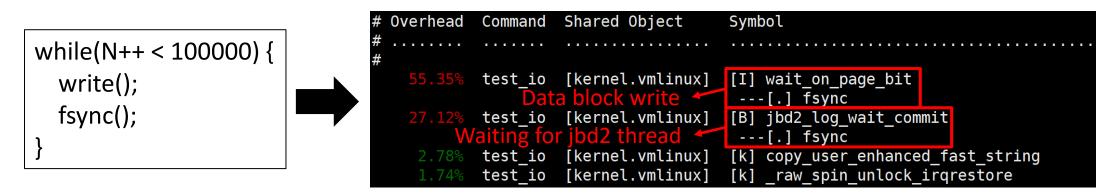
Blocked Samples

- Collected information
 - IP and callchain
 - Off-CPU subclass: reason for the blocking
 - Blocking I/O, synchronization, CPU scheduling, etc.
 - New subclasses can be defined as needed
 - Weight: # of repeats
 - Encode the number of blocked samples with the same attributes



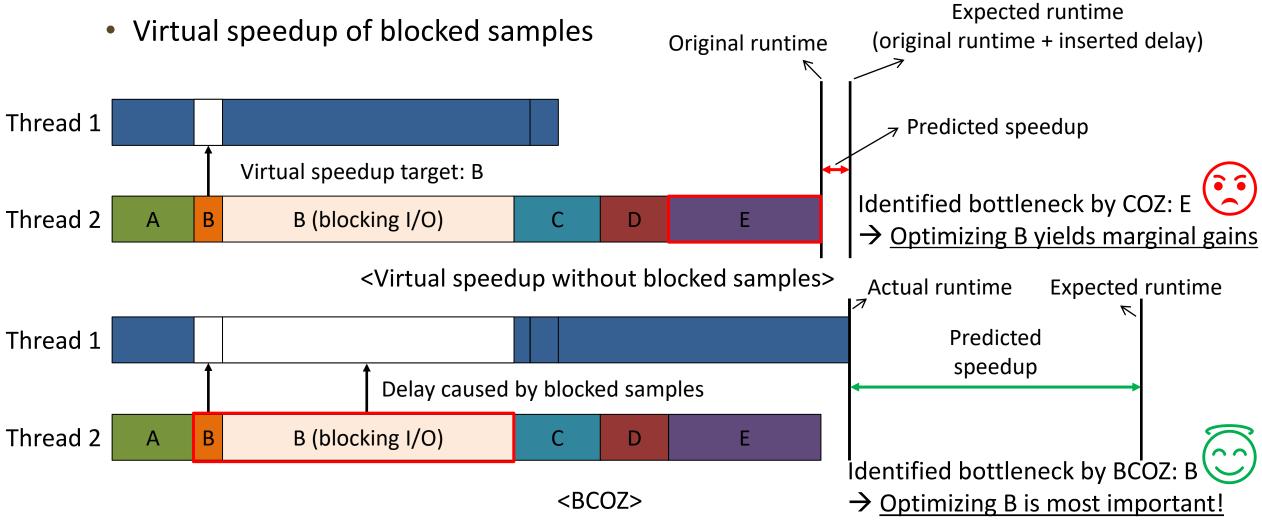
bperf: Statistical Profiler on Both On-/Off-CPU Events

- Extension of Linux perf tool to support blocked samples
 - Sample accounting
 - bperf accounts blocked samples with on-CPU samples on the same dimension
 - bperf classifies samples considering IP, callchain, and <u>subclasses</u> of blocked samples
 - Result reporting
 - <u>New symbol annotations</u> for blocked samples
 - [I]: blocking I/O, [L]: synchronization, [S]: CPU scheduling, [B]: others
 - Both the last user-level IP and last kernel-level IP are reported for blocked samples
 - Enables an in-depth understanding of off-CPU events



BCOZ: Causal Profiler on Both On-/Off-CPU Events

• Extension of COZ to support blocked samples



Features and Challenges of BCOZ

- Features
 - Sampling kernel codes
 - Virtual speedup of blocked samples
 - Subclass-level virtual speedup
- Challenges
 - Conflicts with optimization of original COZ
 - Dependency handling + batch processing of samples

\rightarrow For more details, please refer to the paper

Experimental Setup

- CPU: Intel Xeon Gold 5218 2.30GHz * 2
- OS: Ubuntu 20.04 Server (Linux kernel version: 5.3.7)
- Memory: DDR4 2933MHz, 384GB
- Storage devices: Samsung NVMe PM1735 (1,500K IOPS)
- Questions:
 - Q1) Can blocked samples identify true bottlenecks?
 - Q2) Differences from wPerf's results?
 - Q3) Profiling overhead?
 - Comparison of tracing (off-CPU only), sampling (on-CPU only), bperf (both on-/off-CPU)
 - BCOZ overhead analysis

\rightarrow Please refer to the paper

Summary of the Profiling Results

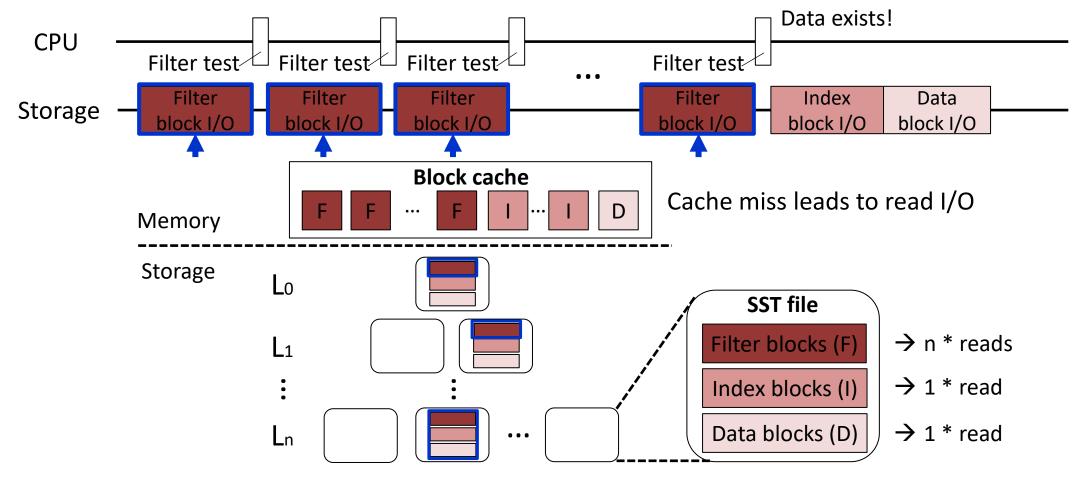
• Results included in the paper

Benchmark	Workload	Identified bottlenecks	Optimization	Speedup?	Known solution?	
	prefix_dist	Block cache contention	- Sharding	O (3.4x)	Yes	Case study 2
	allrandom	Block read I/O	- Asynchronous I/O	O (1.8x)	No	Case study 1
RocksDB	fillrandom	Compaction, write stall	 No block compression Increase the number of compaction thread Reduce write stall 	O (2.6x)	Yes	
NPB	Integer sort	CPU contention	- Allocate more CPU cores	O (16.4x)	Yes	

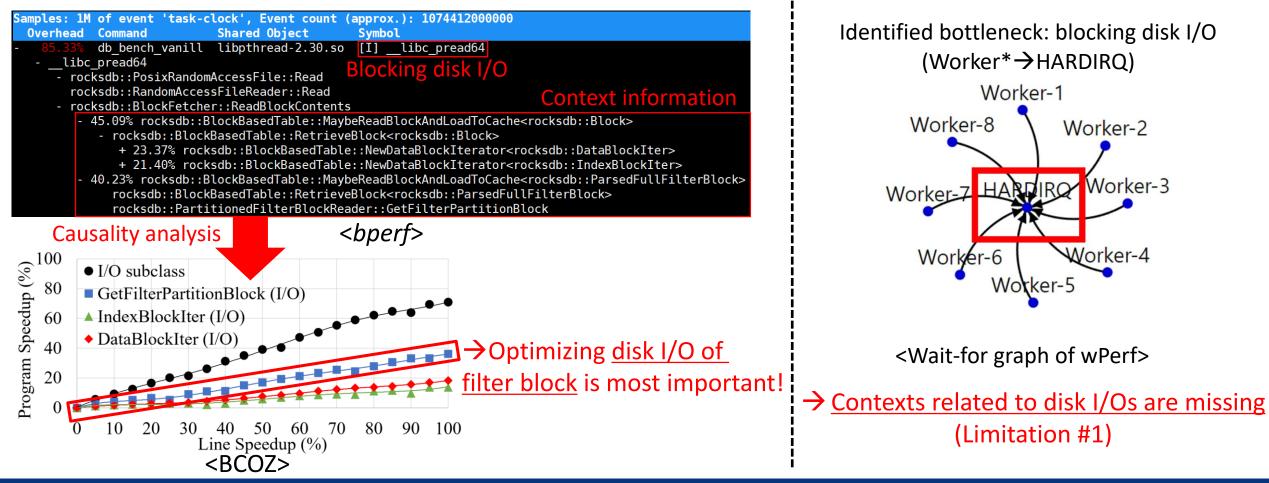
• Results not included in the paper (optimization is ongoing)

Benchmark	Identified Bottlenecks		
HPCG	Serialized SYMGS (Symmetric Gauss Seidel) kernel		
LLaMA-cpp	Blocking I/O in <i>ggml_vec_dot</i>		

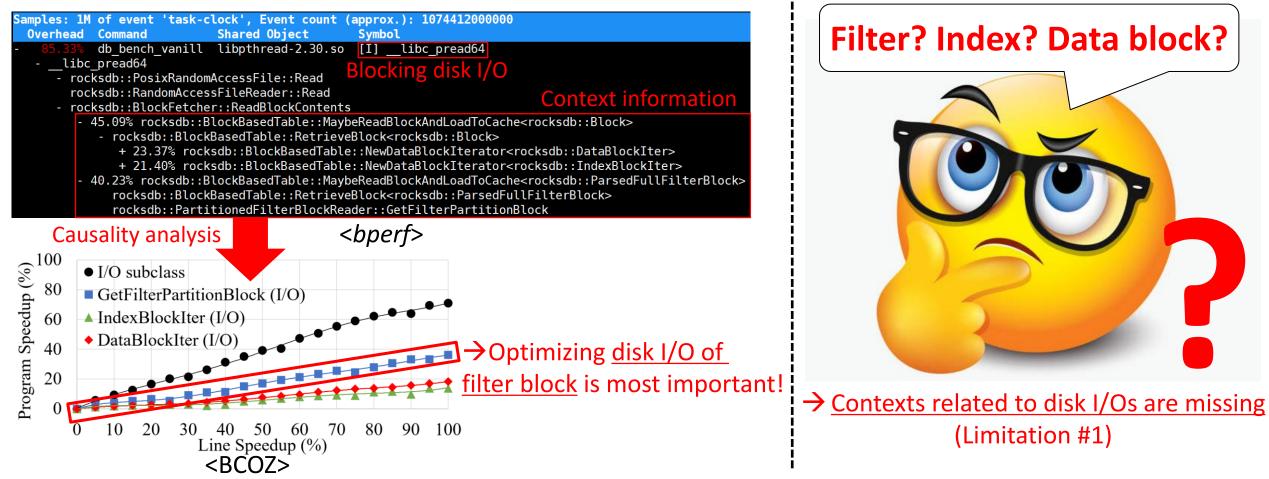
- Scenario: read-only workload (*allrandom*), small block cache (0.1% of dataset size)
- Problem: frequent block (filter, index, data) read I/Os



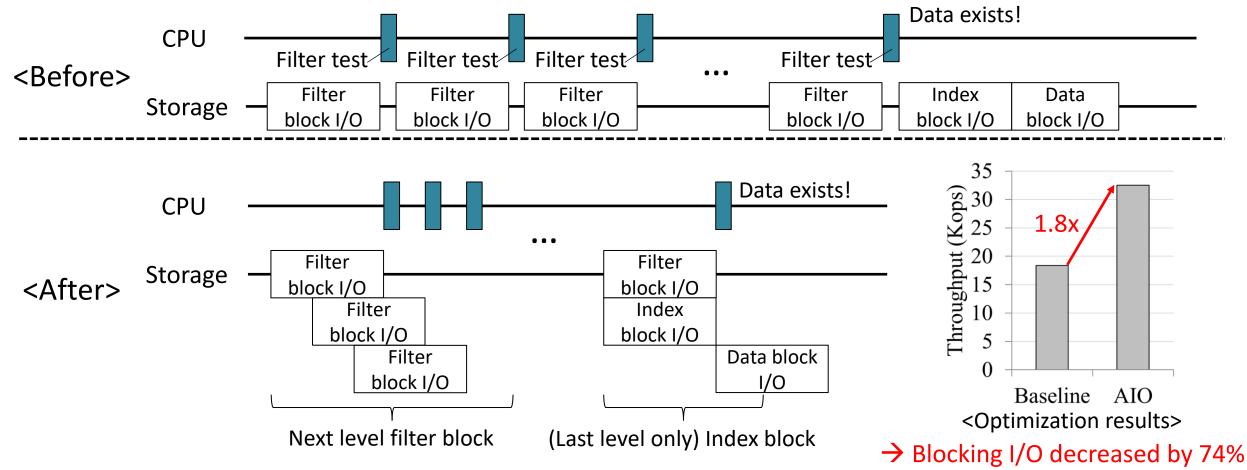
- Scenario: read-only workload (allrandom), small block cache (0.1% of dataset size)
- Identified bottlenecks: blocking disk I/O (filter, index, and data blocks)



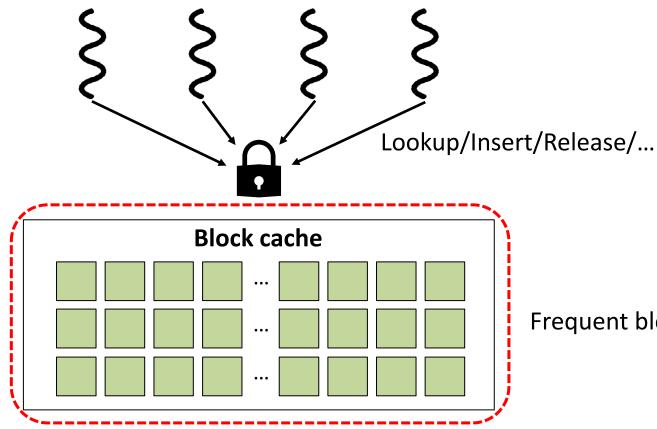
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- Scenario: read-only workload (allrandom), small block cache (0.1% of dataset size)
- Optimization: asynchronous I/O for filter and index blocks

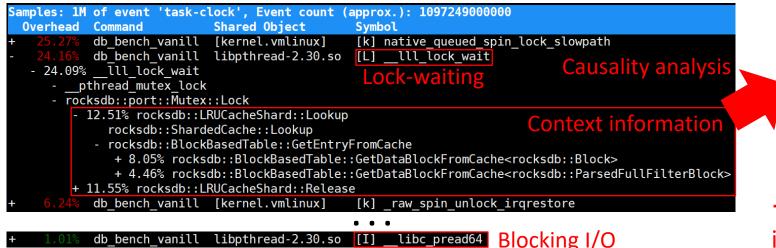


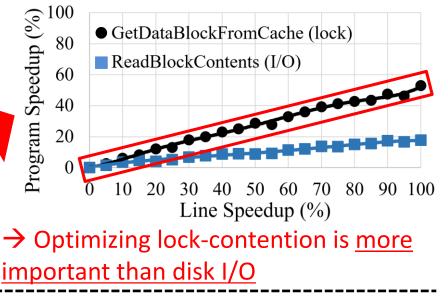
- Scenario: read-only workload (prefix_dist), large block cache (10% of dataset size)
- Problem: block cache lock contention



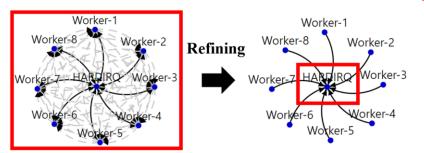
Frequent block cache access leads to lock contention

- Scenario: read-only workload (*prefix_dist*), large block cache (10% of dataset size)
- Identified bottlenecks: lock-waiting





Identified bottleneck: blocking disk I/O, lock-waiting (Worker* \rightarrow HARDIRQ, Worker* $\leftarrow \rightarrow$ Worker*)



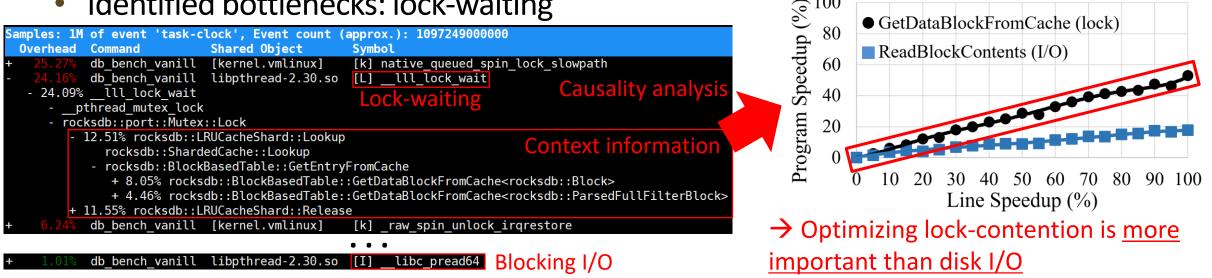
(Limitation #1)

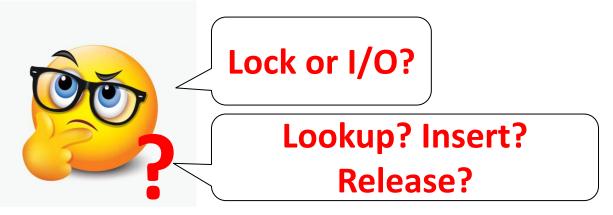
ightarrow Codes that invoke lock-contention are missing

(Limitation #2)

 \rightarrow Actual impact of optimizing blocking disk I/O is missing

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- Identified bottlenecks: lock-waiting





(Limitation #1)

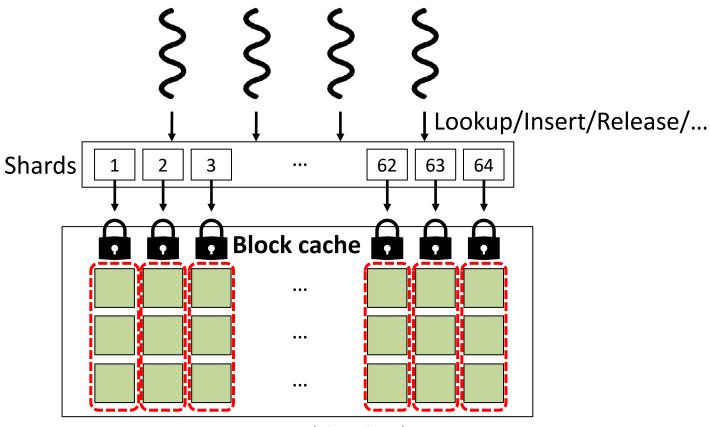
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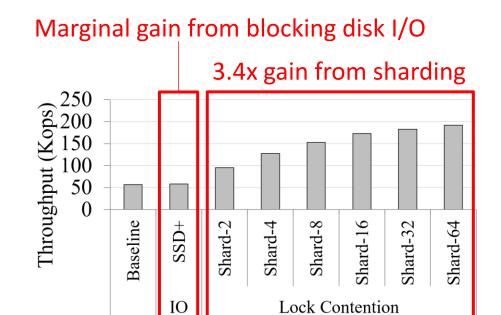
(Limitation #2)

 \rightarrow Actual impact of optimizing blocking disk I/O is missing

- Scenario: read-only workload (prefix_dist), large block cache (10% of dataset size)
- Optimization: apply sharding



<Optimization (sharding)>



 \rightarrow Lock-contention decreased by 97%

<Optimization results>

Conclusion

- Profiling modern applications has become more challenging
- Blocked samples collects off-CPU events information
 - *bperf*, provides <u>statistical profiling</u> of both on-/off-CPU events
 - **BCOZ**, provides <u>virtual speedup</u> of both on-/off-CPU events
- Blocked samples, a general solution for off-CPU sampling
 - Planning on <u>enriching blocked samples</u> with off-CPU information details (device-internal ops.)

Blocked samples is available at:

https://github.com/s3yonsei/blocked_samples







