GoFetch:

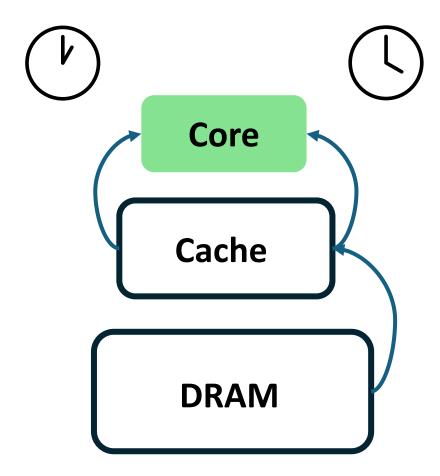
Breaking Constant-Time Cryptographic Implementations Using Data Memory-Dependent Prefetchers

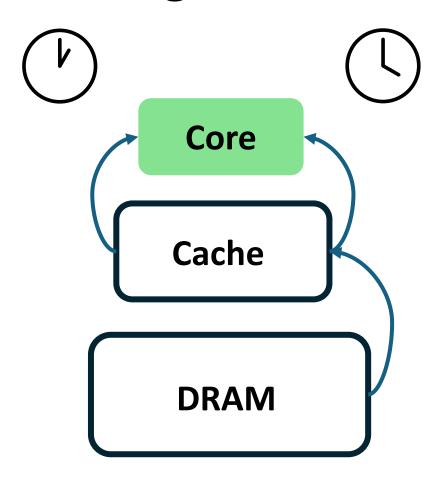
Boru Chen, Yingchen Wang, Pradyumna Shome,

Christopher Fletcher, David Kohlbrenner, Riccardo Paccagnella,

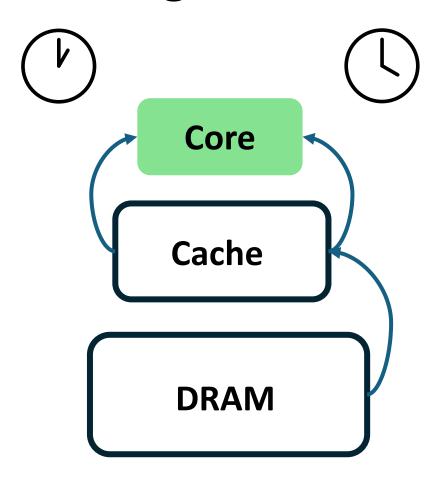
Daniel Genkin

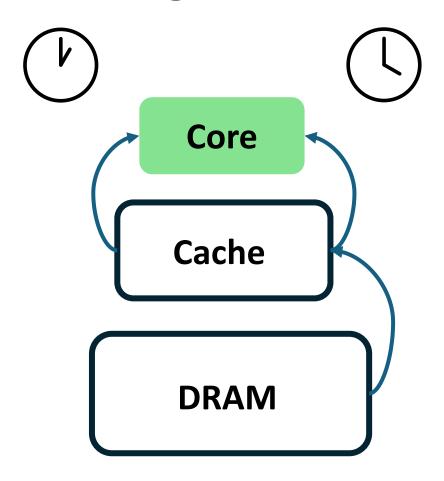




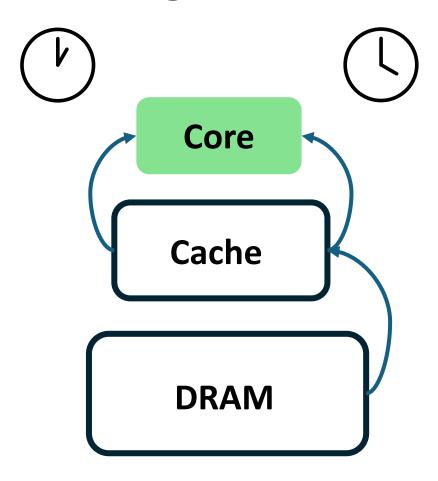


```
// secret = 1 or 0
if (secret)
{
  trash = *addr_A
}
```

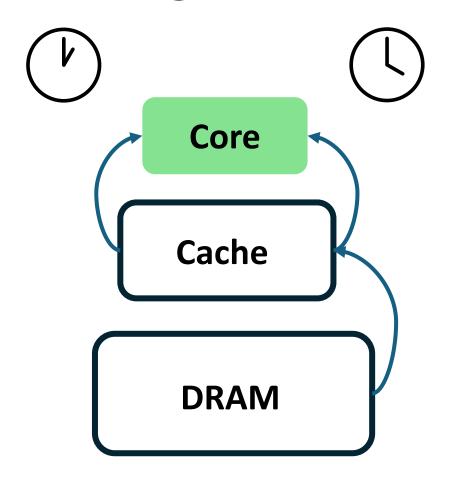




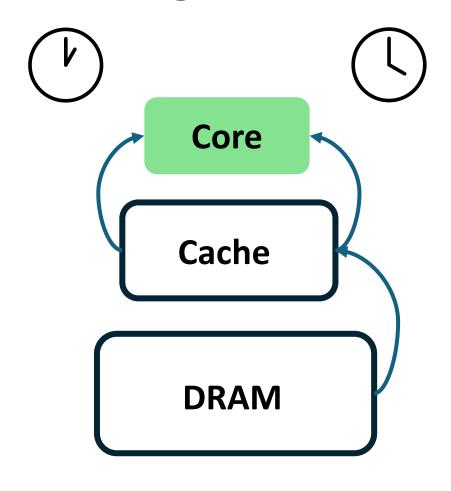
```
Flush
// secret = 1 or O Cache
 if (secret)
   trash = *addr A
                         Load
         Cache
```



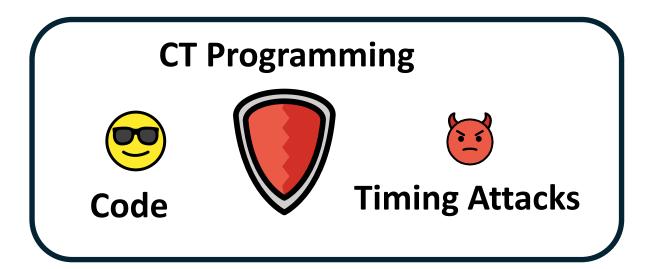
```
Flush
// secret = 1 or O Cache
if (secret)
  trash = *addr_A
                          Load
                          addr A
    Α
        Cache
 secret = 1
```

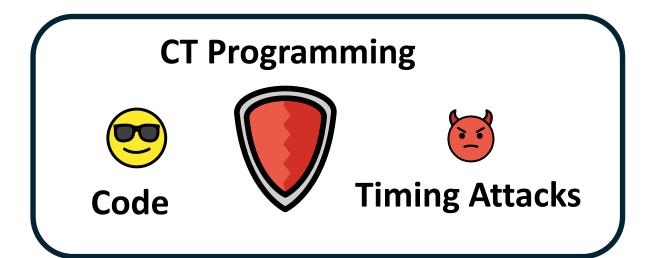


```
Flush
// secret = 1 or O Cache
if (secret)
  trash = *addr_A
                          Load
                          addr_A
        Cache
 secret = 1
```

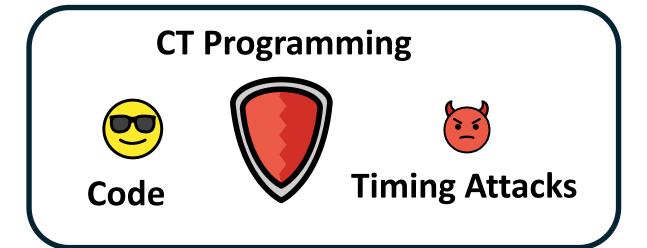


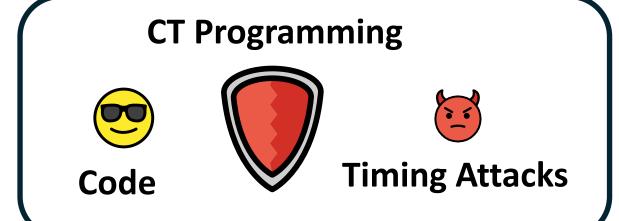
```
Flush
// secret = 1 or O Cache
if (secret)
  trash = *addr A
                           Load
                           addr_A
         Cache
                  Α
 secret = 1
               secret = 0
```

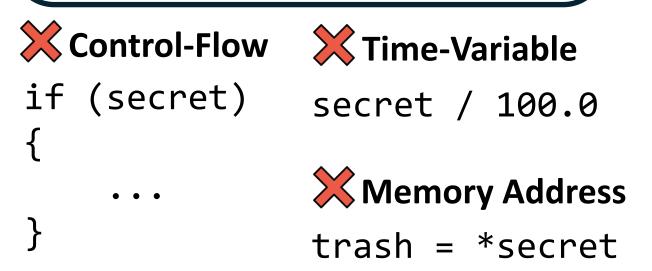




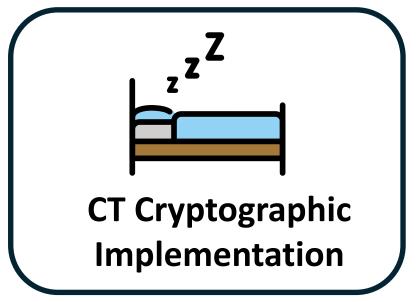












This talk:

Show that these principles are insufficient.



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Show that these principles are insufficient.





Data Memory
Dependent Prefetcher

This talk:

Show that these principles are insufficient.





Data Memory
Dependent Prefetcher

```
secret = *non-sec-addr
secret = *non-sec-addr
secret = *non-sec-addr
secret = *non-sec-addr
```

program without secret-dependent load

This talk:

Show that these principles are insufficient.





Data Memory Dependent Prefetcher

```
secret = *non-sec-addr
secret = *non-sec-addr
secret = *non-sec-addr
                                             *secret
secret = *non-sec-addr
                                        *secret
         program without
```

secret-dependent load

This talk:

Show that these principles are insufficient.



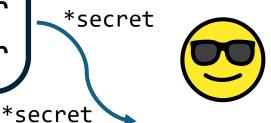


Data Memory
Dependent Prefetcher

```
secret = *non-sec-addr
secret = *non-sec-addr
secret = *non-sec-addr
secret = *non-sec-addr
```

program without secret-dependent load

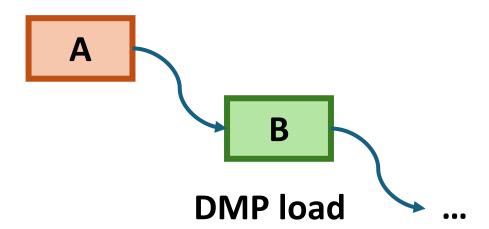
Cache Timing Attacks



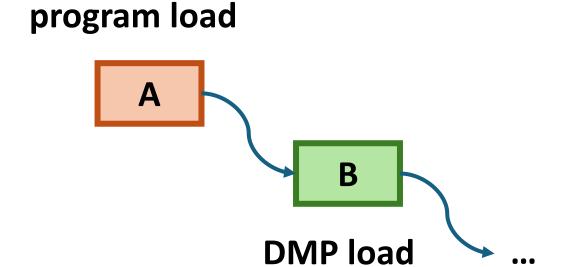
Apple DMP could treat loaded data as memory address and perform access.

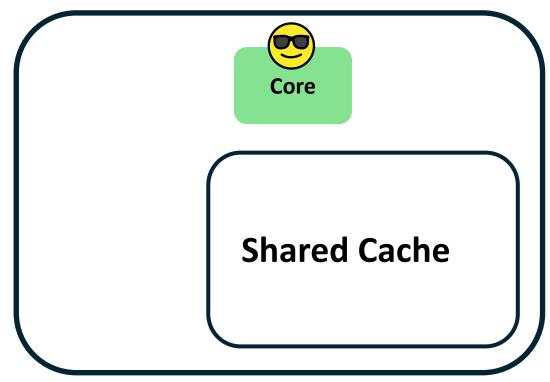
Apple DMP could treat loaded data as memory address and perform access.

program load

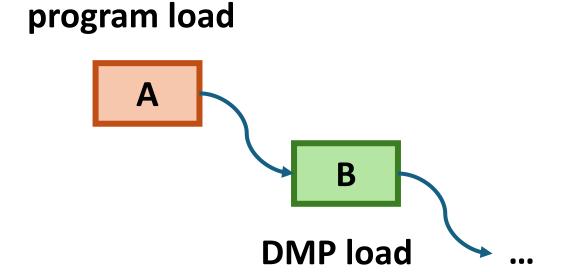


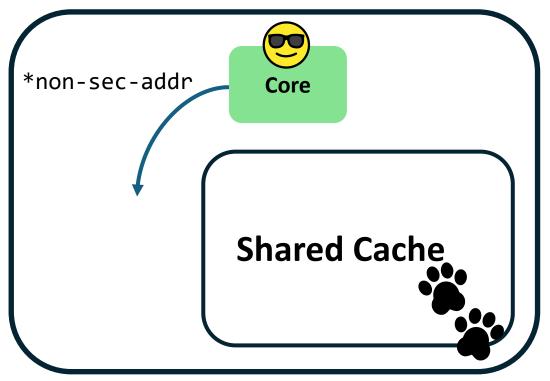
Apple DMP could treat loaded data as memory address and perform access.



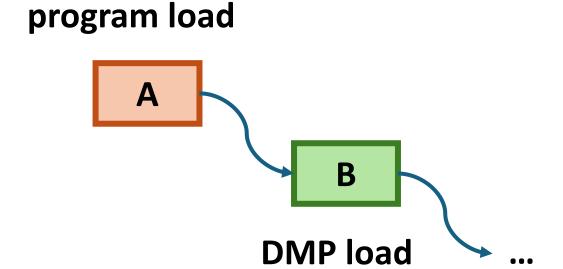


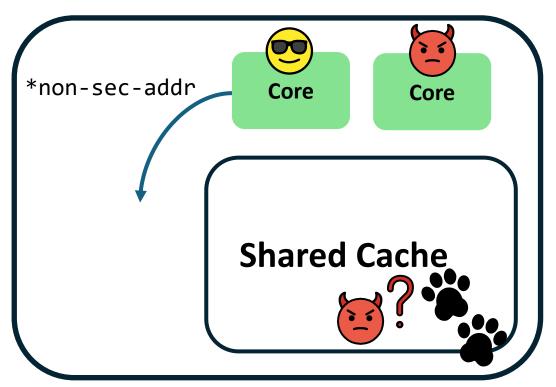
Apple DMP could treat loaded data as memory address and perform access.



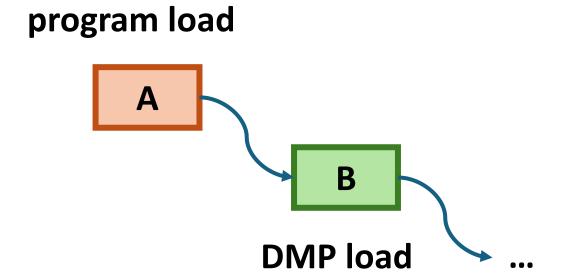


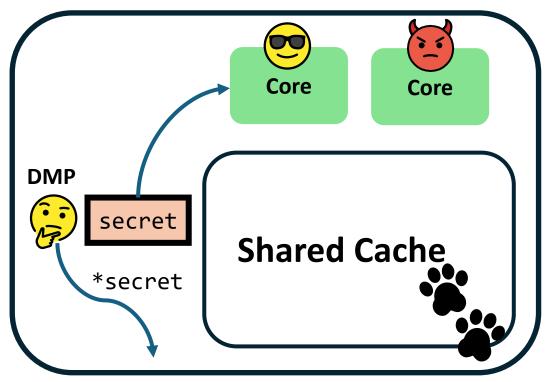
Apple DMP could treat loaded data as memory address and perform access.



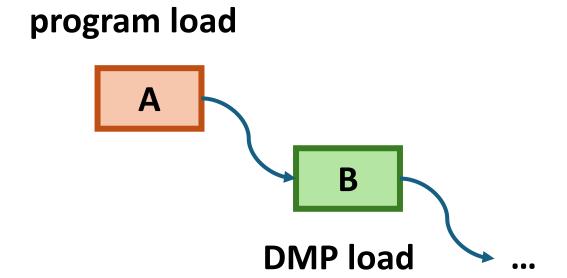


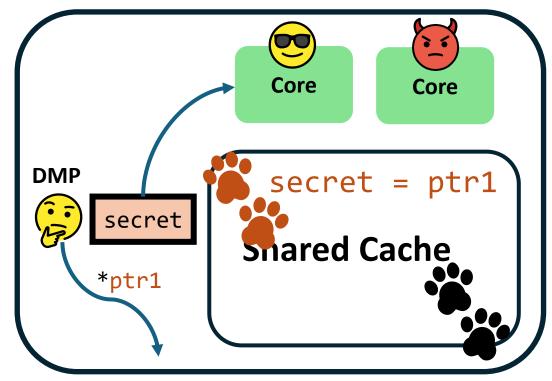
Apple DMP could treat loaded data as memory address and perform access.



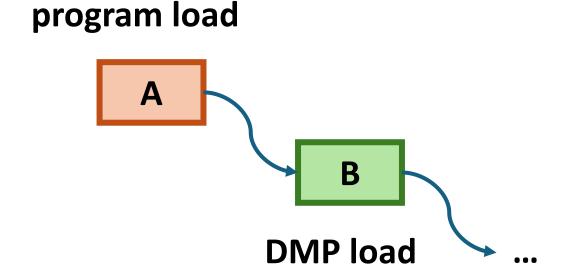


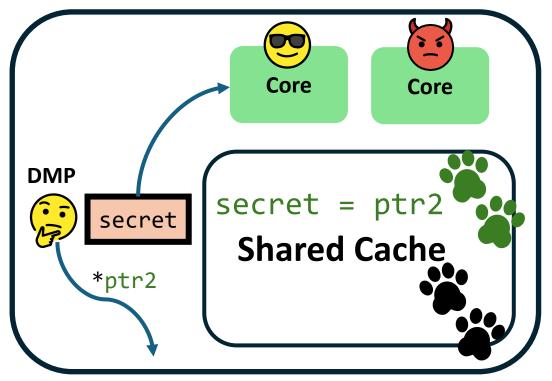
Apple DMP could treat loaded data as memory address and perform access.



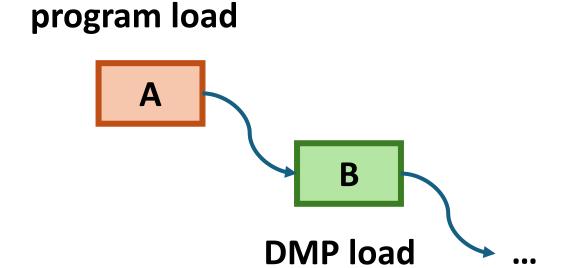


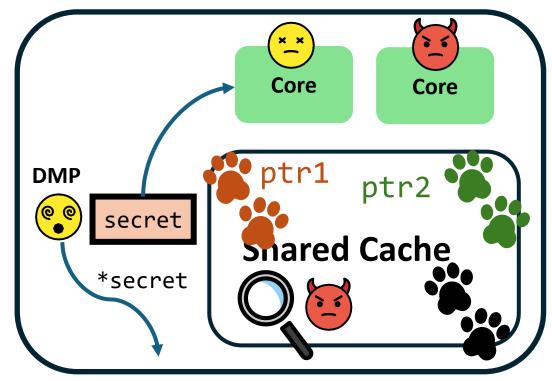
Apple DMP could treat loaded data as memory address and perform access.





Apple DMP could treat loaded data as memory address and perform access.





Augury¹

• Comprehensive reverse engineering of Apple DMPs.



- Comprehensive reverse engineering of Apple DMPs.
- Develop DMP-aided choseninput attack framework.



- Comprehensive reverse engineering of Apple DMPs.
- Develop DMP-aided choseninput attack framework.
- Undermine four cryptographic implementations in the wild or submitted to NIST PQC standardization.



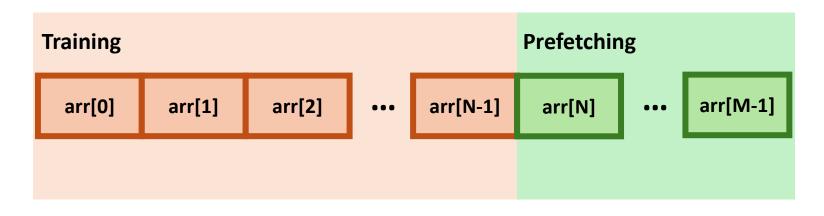
How do classical prefetchers work?

How do classical prefetchers work?

```
// stride pattern
for (i = 0; i < M; i++)
    trash += arr[i];</pre>
```

The program reads arr[0], arr[1], ...

The stride is 1! Prefetch arr[N], arr[N+1],...





What is the finding of prior work, Augury?

```
// Array-of-pointer pattern
for (i = 0; i < M; i++)
    trash += *arr[i];</pre>
```

Augury

Dereferenced by code

Dereferenced by DMP

DMP recognizes and

prefetches Array-of-

pointers access pattern!

Does memory access pattern even matter?

```
// Array-of-pointer pattern
for (i = 0; i < M; i++)
    trash += *arr[i];</pre>
```

Really? Is it necessary?

GoFetch

Does memory access pattern even matter?

Really? Is it necessary?

GoFetch

```
// Array-of-pointer pattern
for (i = 0; i < M; i++)
    trash += *arr[i];
    // Single load
    trash += arr[0];
   Load
        ----> Dereferenced by DMP
 arr[0]
```

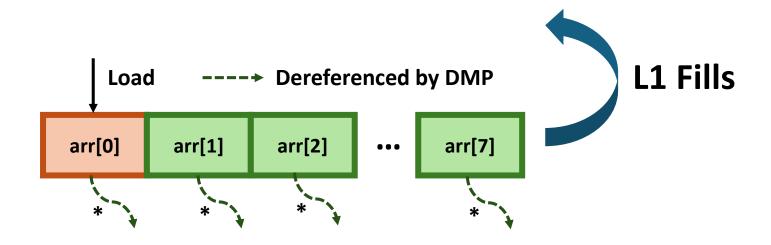
Does memory access pattern even matter?



```
// Array-of-pointer pattern
for (i = 0; i < M; i++)
     trash += *arr[i];
    // Single load
    trash += arr[0];
    Load
         ---→ Dereferenced by DMP
 arr[0]
        arr[1]
              arr[2]
                        arr[7]
                    •••
```

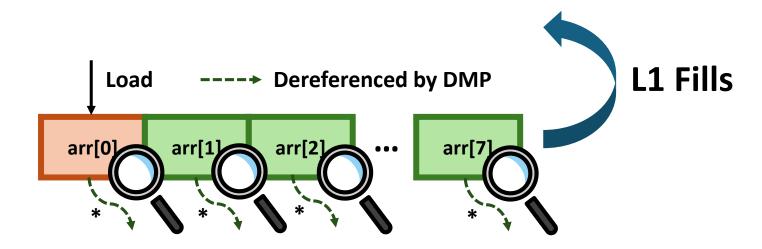
Where does the DMP scan for pointers?

```
// Single load
trash += arr[0];
```



Cache Line Aligned

How does DMP determine pointers to dereference in each line?



Cache Line Aligned

History filter: how DMP avoids redundant

dereference?

4GByte region: heuristic of predicting pointer value.

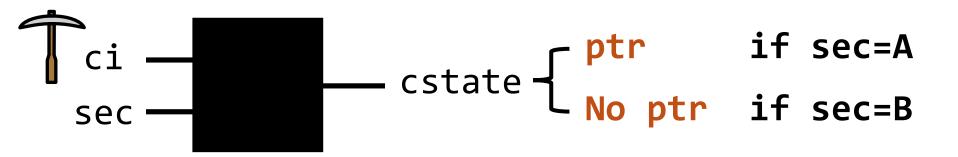
Do-not-scan hint: how DMP avoids redundant scan?

Top byte ignore: how DMP synergizes with TBI?

••

cstate: crypto state

sec: secret ci: chosen input



cstate: crypto state

sec: secret ci: chosen input

cstate = ci AND sec



cstate

sec

```
cstate: crypto state
sec: secret ci: chosen input
cstate = ci AND sec
```



Cryptanalysis for DMP exploit



End-to-end key extraction PoCs

Cryptanalysis for DMP exploit



End-to-end key extraction PoCs

OpenSSL DHKE

Cryptanalysis for DMP exploit



End-to-end key extraction PoCs

OpenSSL DHKE Go RSA

Cryptanalysis for DMP exploit



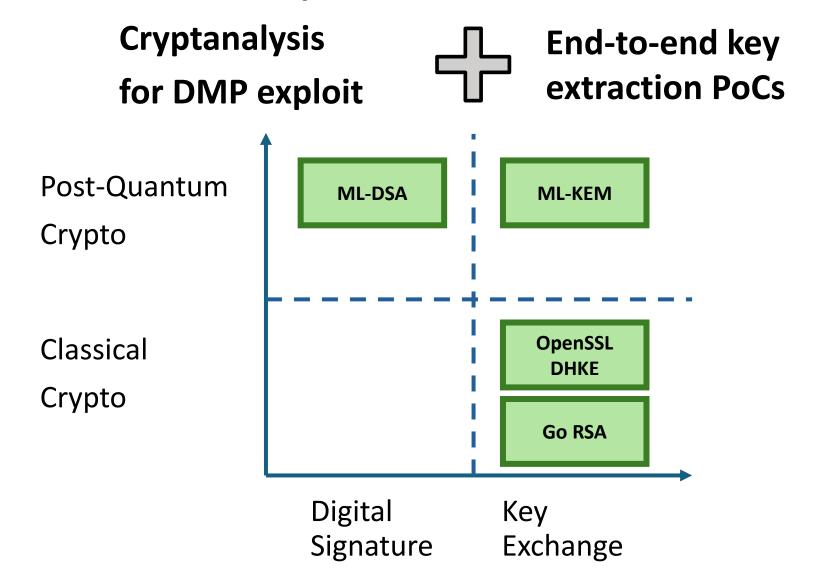
End-to-end key extraction PoCs

ML-DSA

ML-KEM

OpenSSL DHKE

Go RSA





Apple: Disable DMP with DIT=1

Only works on M3.

```
bool set_DIT_enabled(void) {
    bool was_DIT_enabled = get_DIT_enabled();
    __asm__ __volatile__("msr dit, #1");
    return was_DIT_enabled;
}
```

Enable DIT for constant-time cryptographic operations



Apple: Disable DMP with DIT=1

Only works on M3.



Go: Propose an opt-in DIT mode in Go binary.



proposal: runtime: implement a DIT/DOIT mode #66450



rolandshoemaker opened this issue on Mar 21 · 20 comments



Apple: Disable DMP with DIT=1

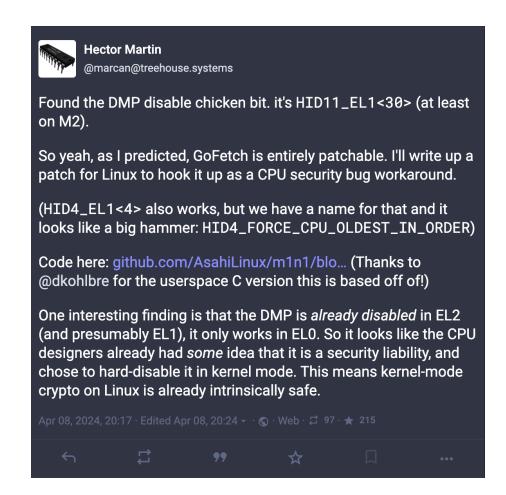
Only works on M3.



Go: Propose an opt-in DIT mode in Go binary.



Asahi Linux: Found chicken bit to disable DMP on M1/M2.





Apple: Disable DMP with DIT=1

Only works on M3.



Go: Propose an opt-in DIT mode in Go binary.





Asahi Linux: Found chicken bit to disable DMP on M1/M2.



Pwine Awards: Best Cryptographic Attack winner.

Conclusion

- Data memory-dependent prefetchers (DMPs) performs secret-dependent memory access to leak data.
- Exploiting DMPs to perform key extraction attacks to constant-time cryptography is feasible.



GoFetch

Check our Website:

gofetch.fail

boruc2@illinois.edu