



Remote Code Execution by Laser Excitation of P–N Junctions

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SERVICE
ENGINE
SOON

Mnemonic	Opcode	Instruction
NOP	0000	no operation
LDA	0001	load accumulator (addr)
INC	0010	increment accumulator
DEC	0011	decrement accumulator
STA	0100	store accumulator (addr)
BZ	0101	branch if A \neq 0 (addr)
JMP	0110	unconditional branch (addr)
SR	0111	shift right accumulator
LDI	1001	load immediate

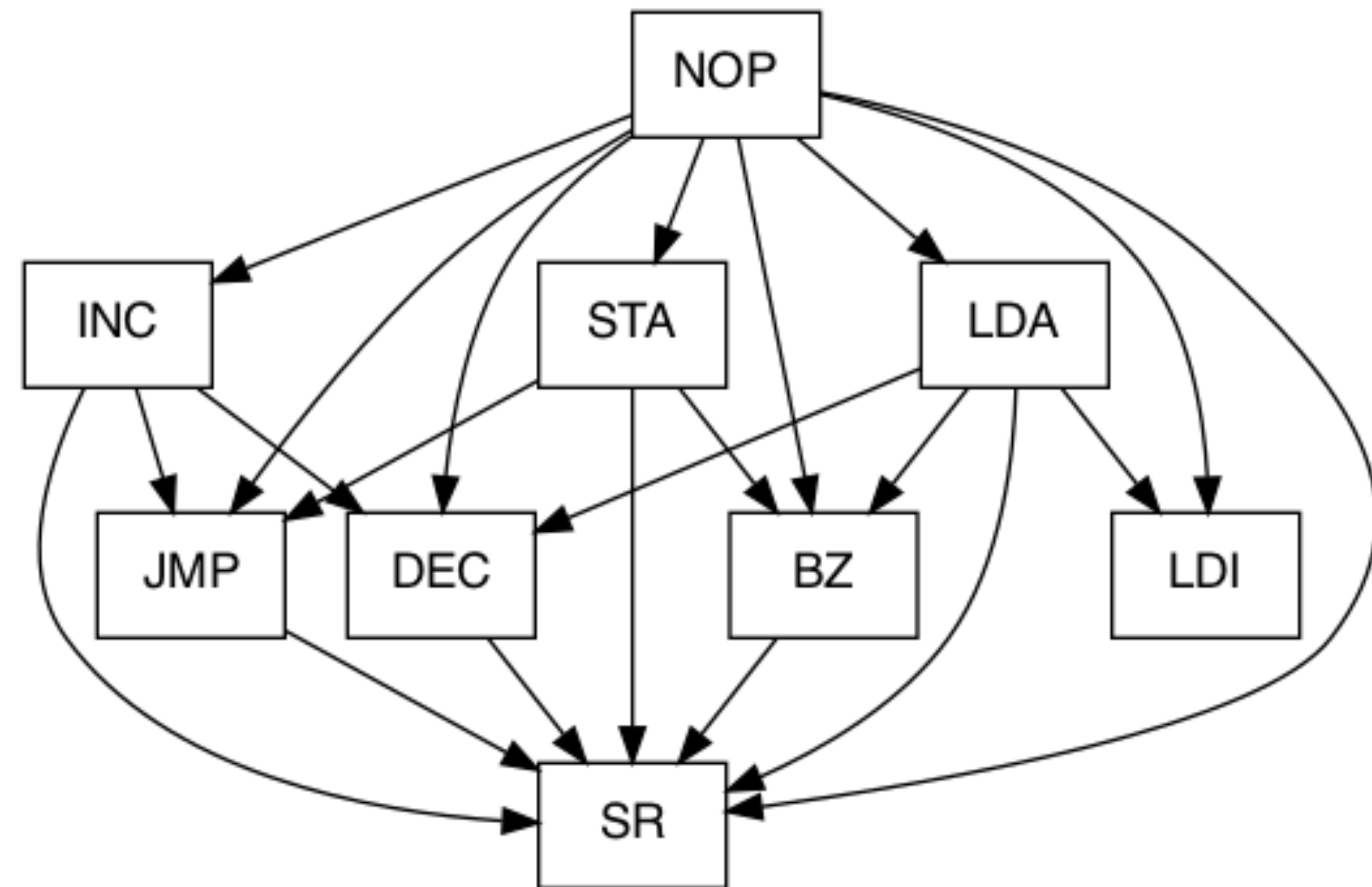
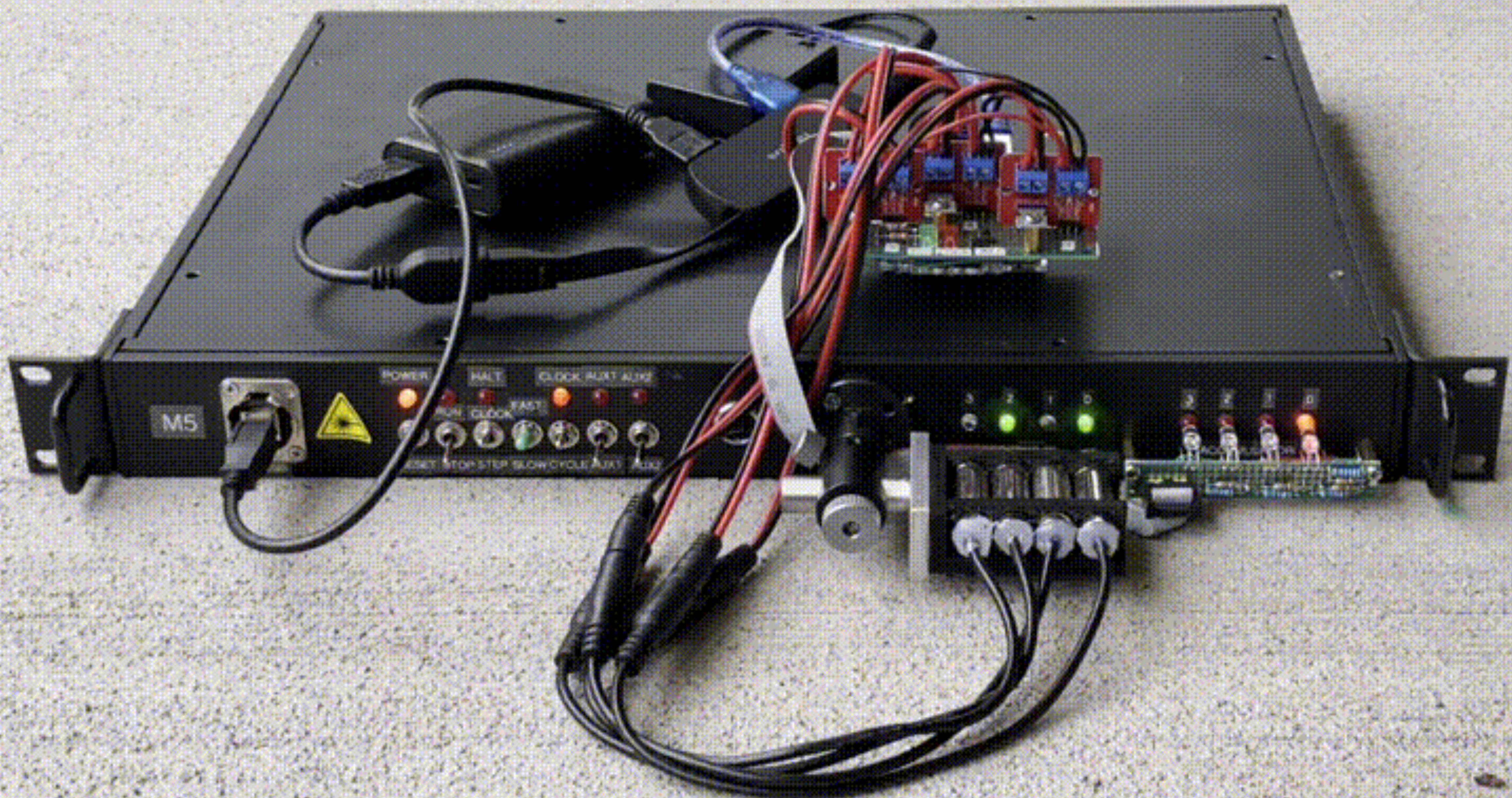
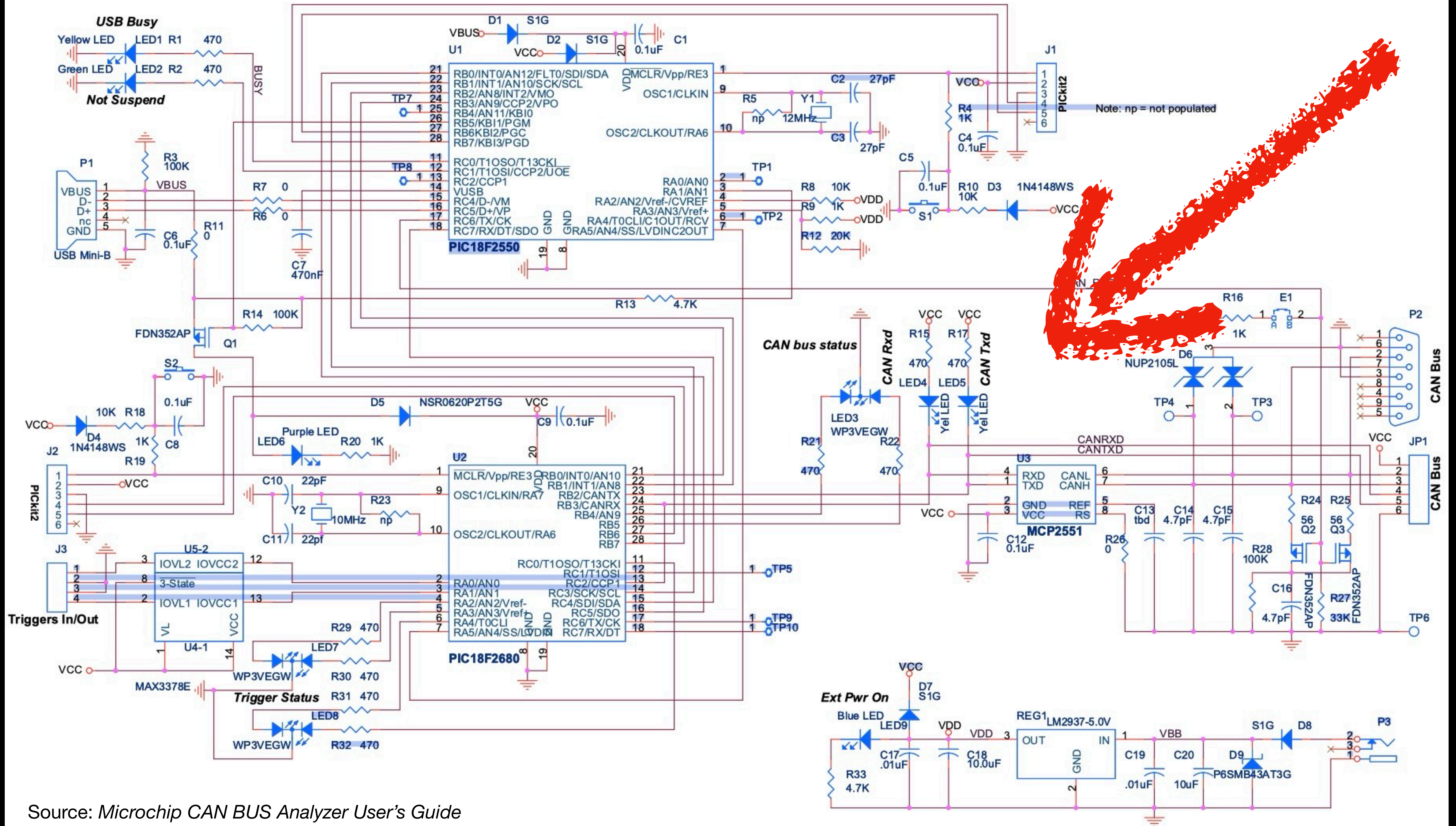


Table 1: M5 instruction set. It consists of 9 instructions including NOP. This is intentionally simple but Turing complete.

Figure 17: Allowable transitions in the instructions set of the computer defined in Table 1 if the attacker can only set bits, but not reset them.





Source: Microchip CAN BUS Analyzer User's Guide

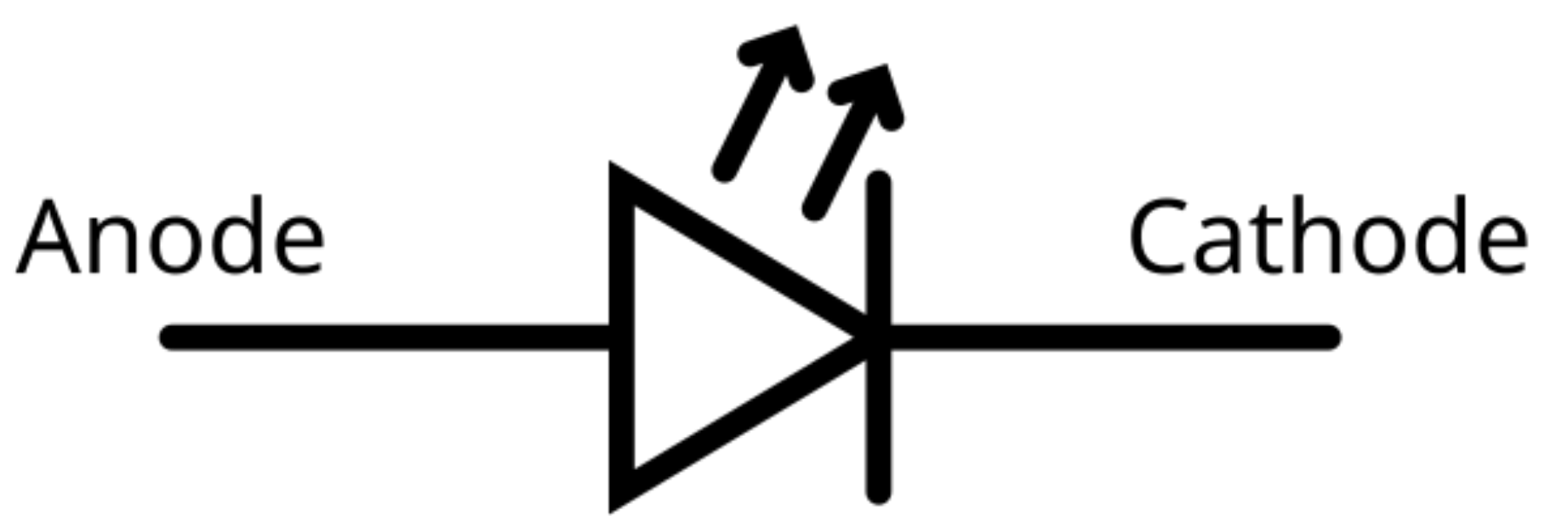
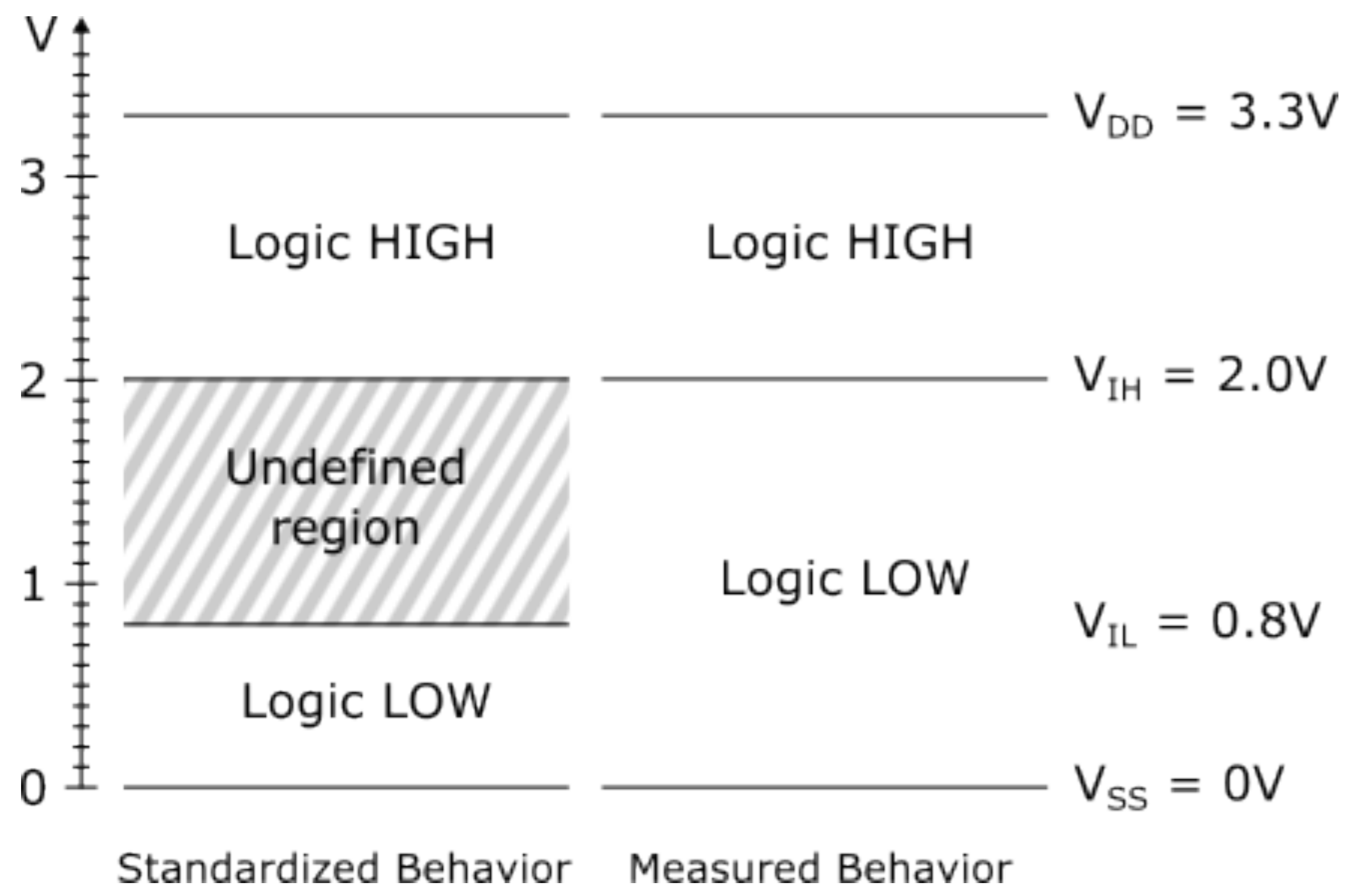
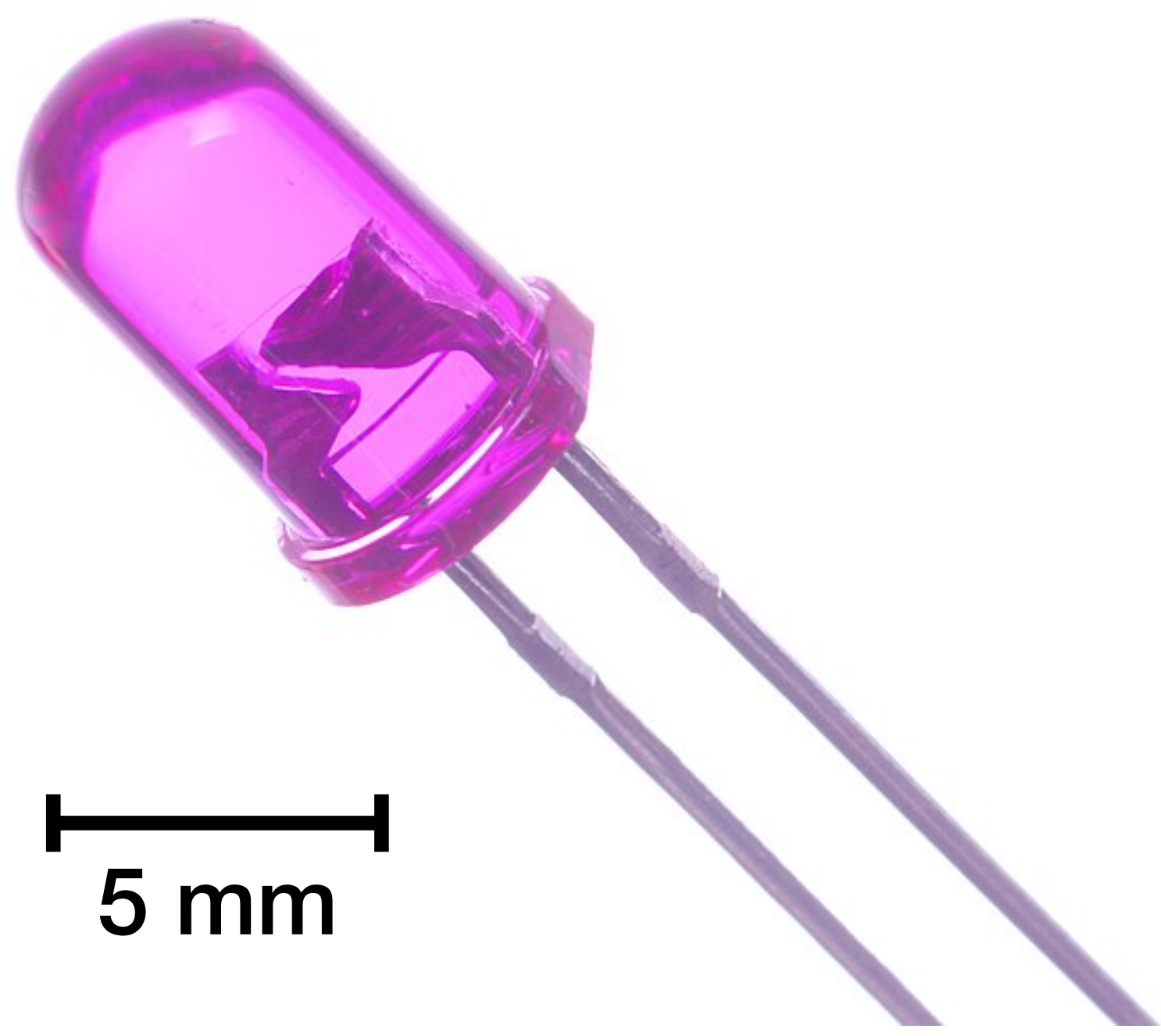
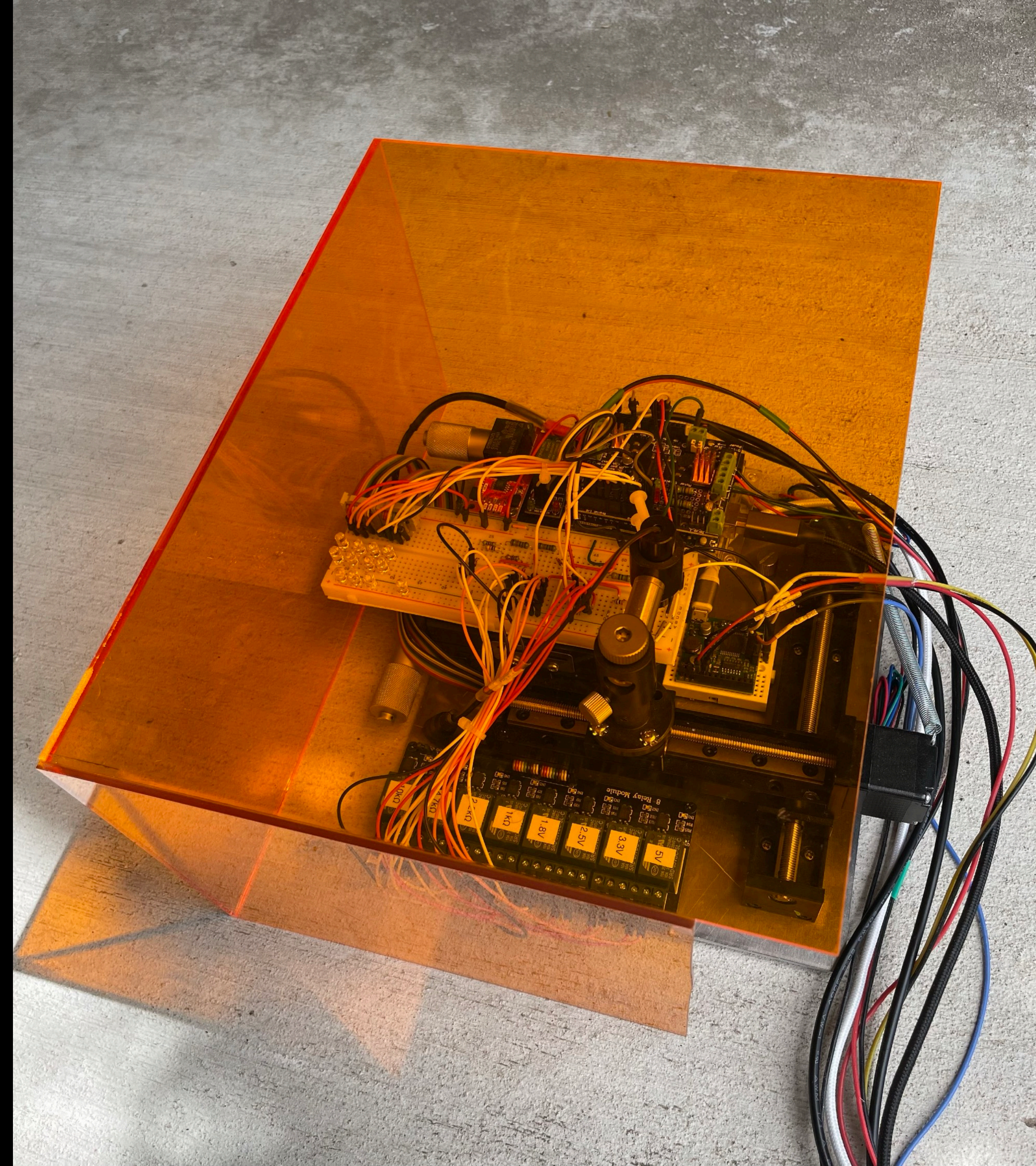
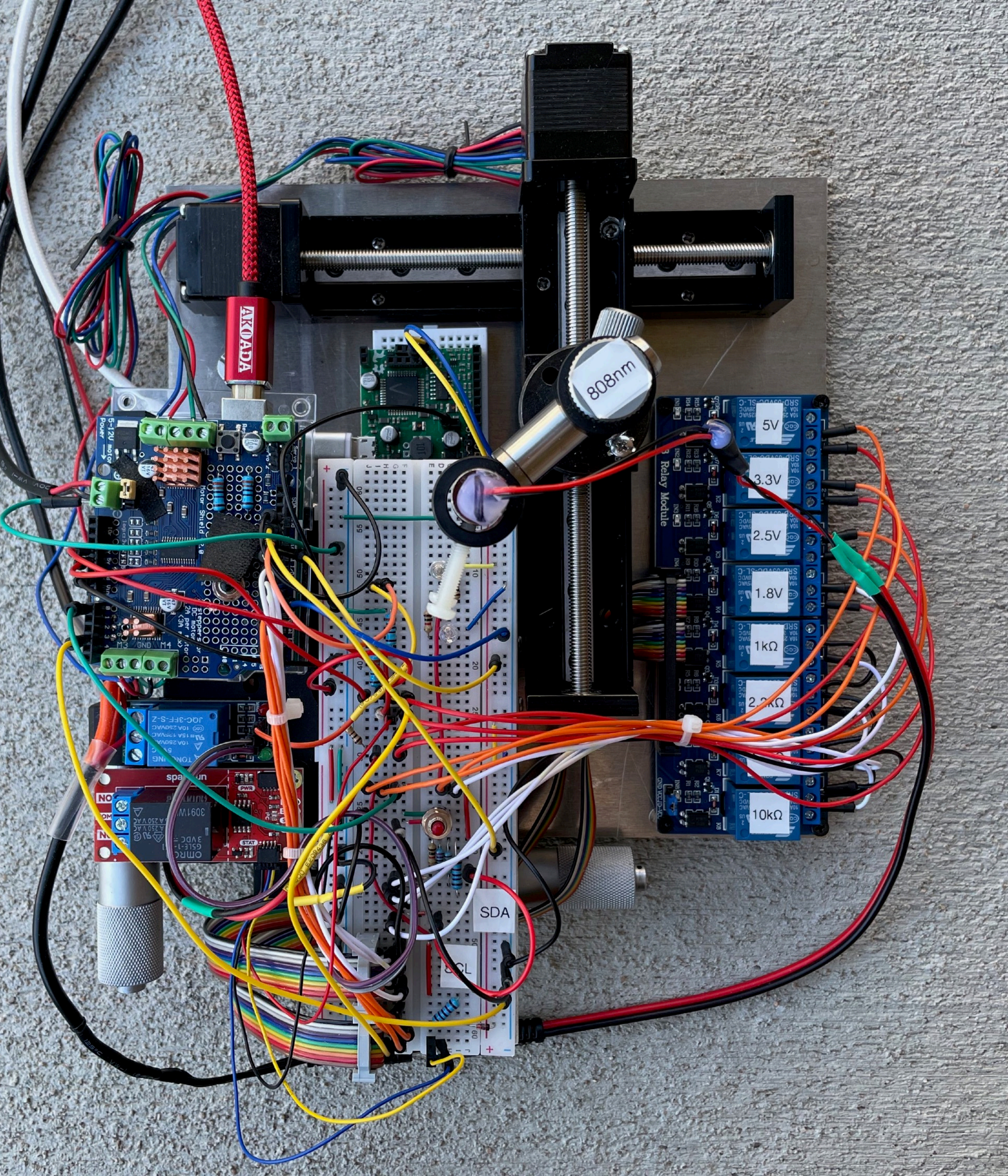
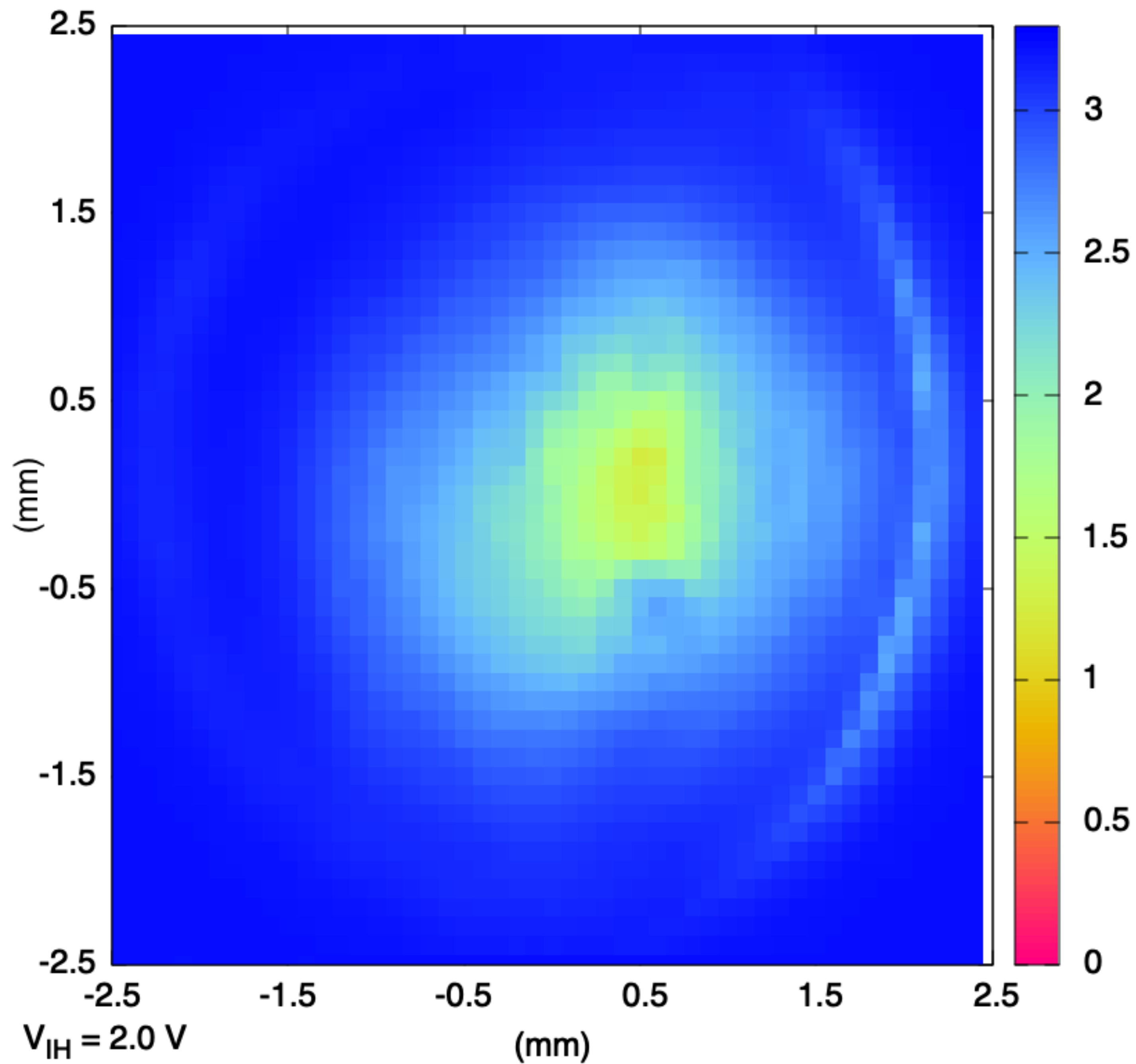


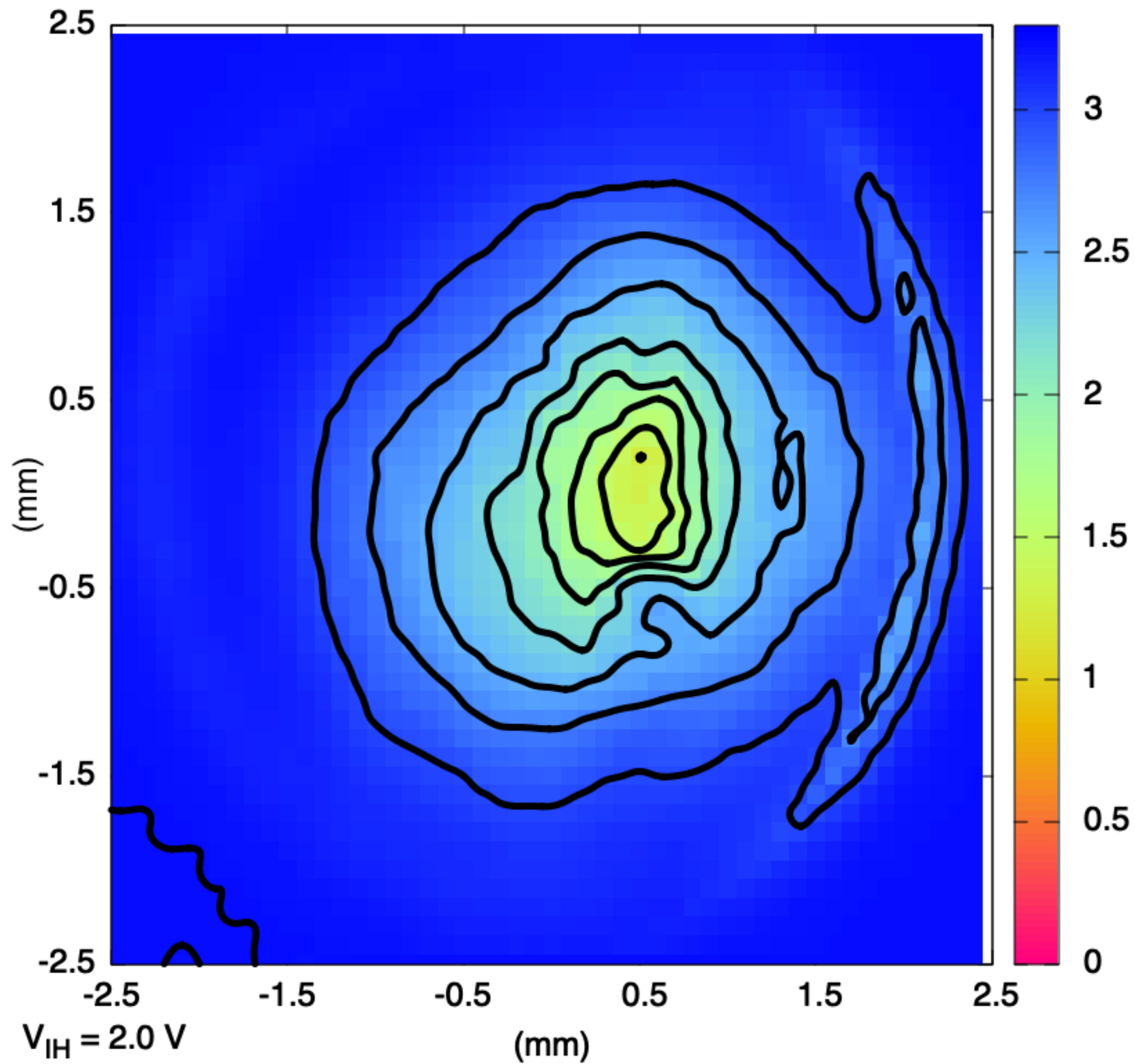
Figure 4. CMOS logic levels for 3.3 V circuits. Signals above 2 V are logic high, and below 0.8 V are logic low. In all our experiments we found that devices will default to a logic low condition in the undefined region, so although it ought never to be used, a signal below 2 V is sufficient for an attack.



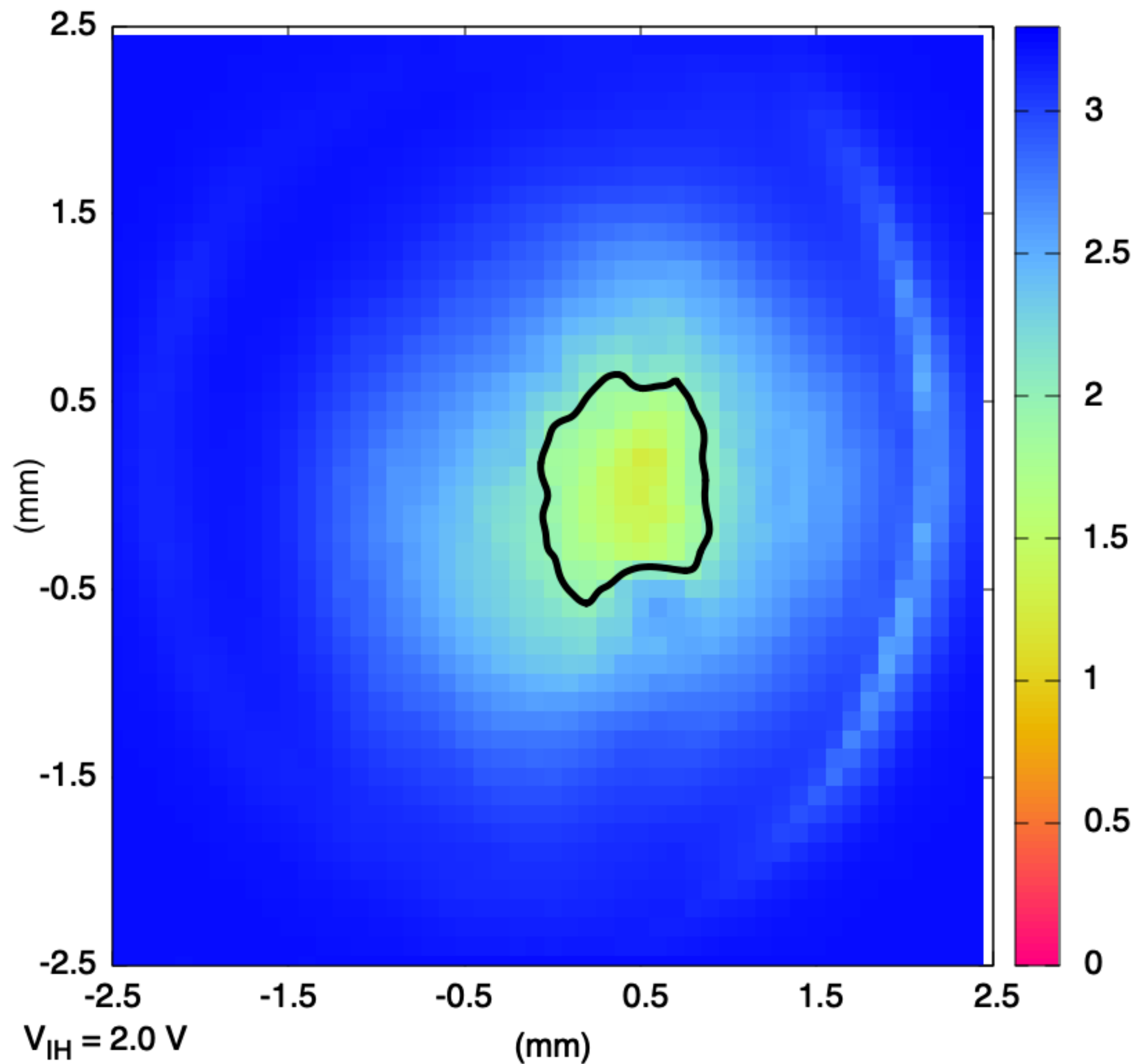
Blue LED, 405 nm laser, 3.3 V logic, 2.2 k Ω pull-up



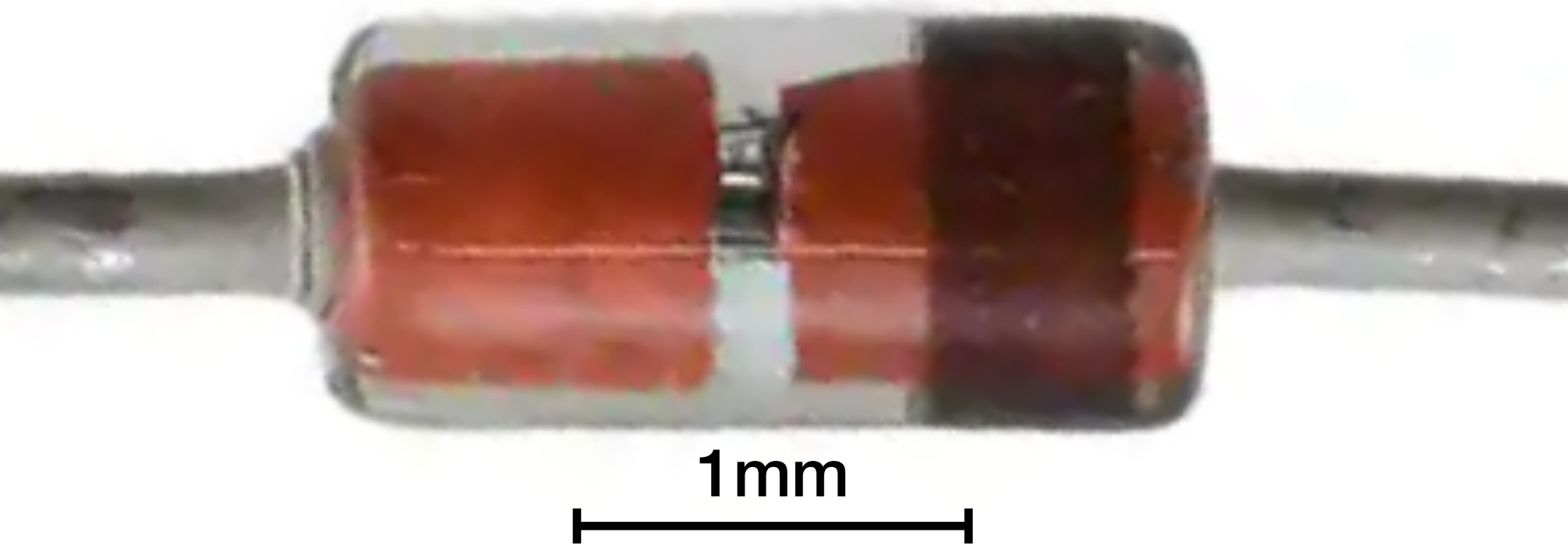
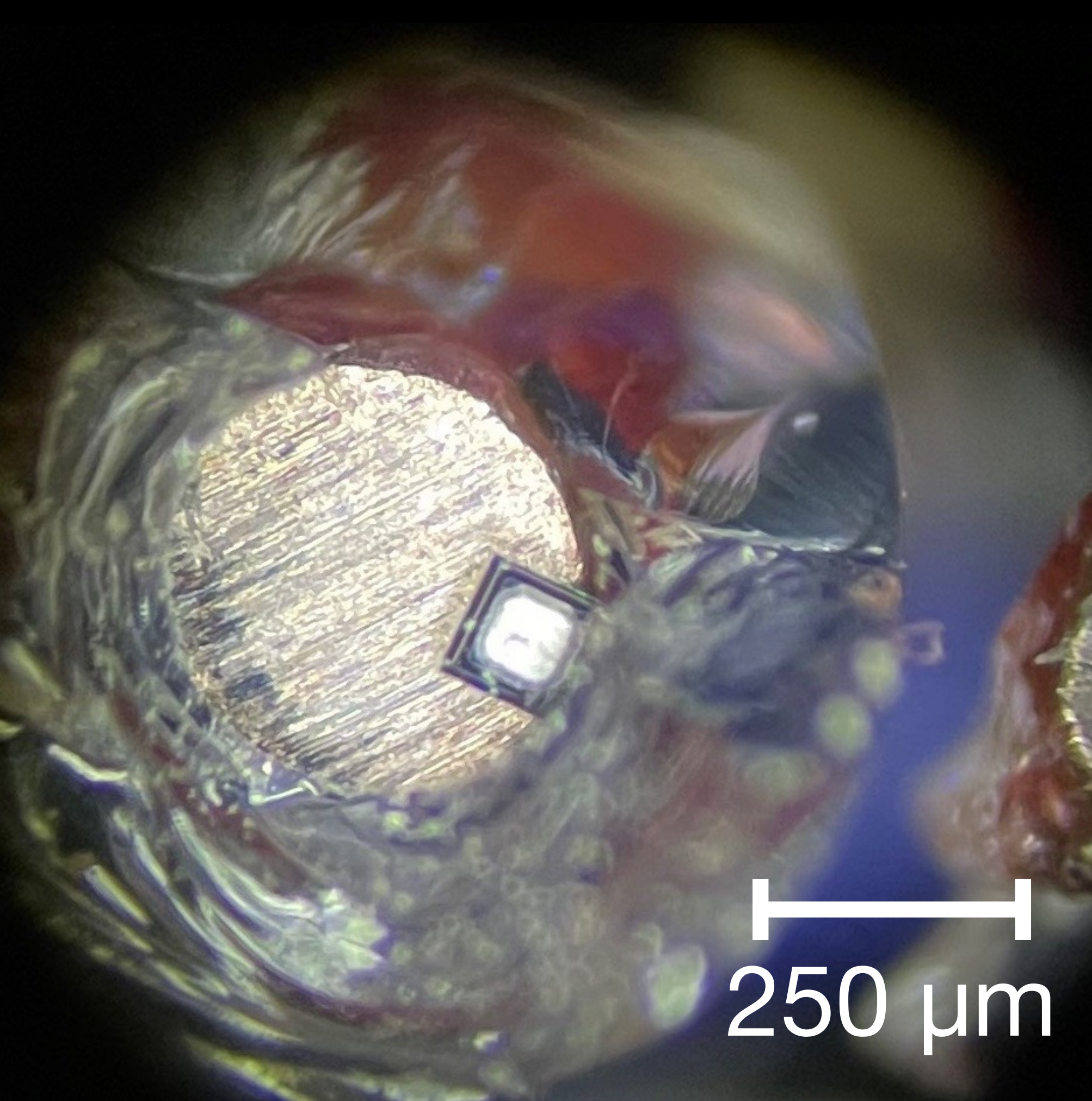
Blue LED, 405 nm laser, 3.3 V logic, 2.2 k Ω pull-up



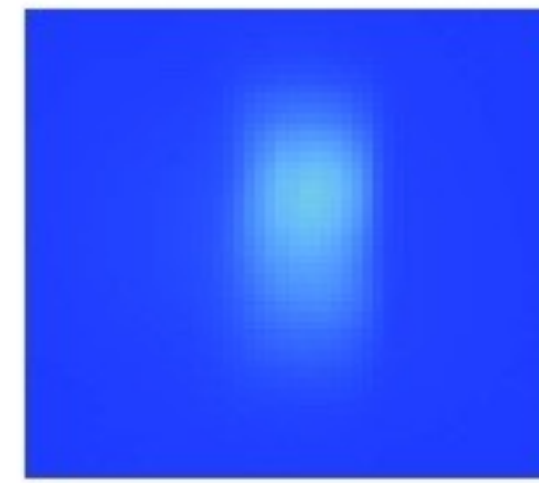
Blue LED, 405 nm laser, 3.3 V logic, 2.2 k Ω pull-up



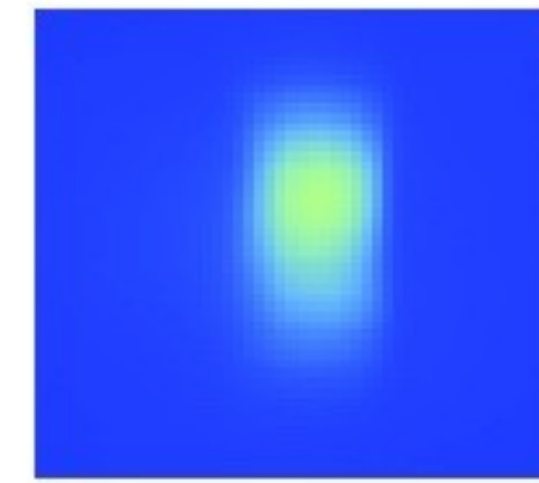
You can't hit a target that isn't there.



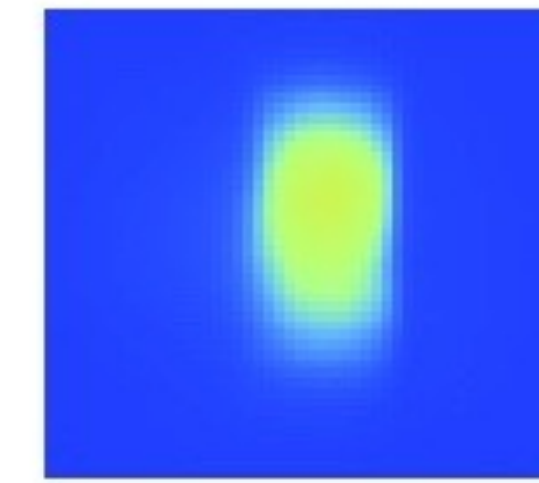
1N34A (Schottky silicon equivalent) DO-35 glass at 808 nm, axis: diagonal



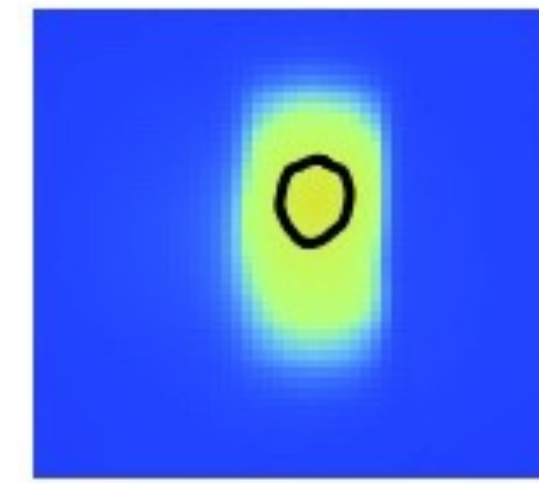
5.0 V, 1k Ω



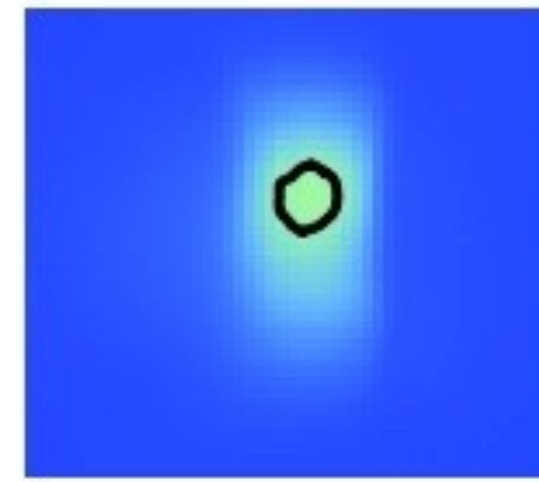
5.0 V, 2.2k Ω



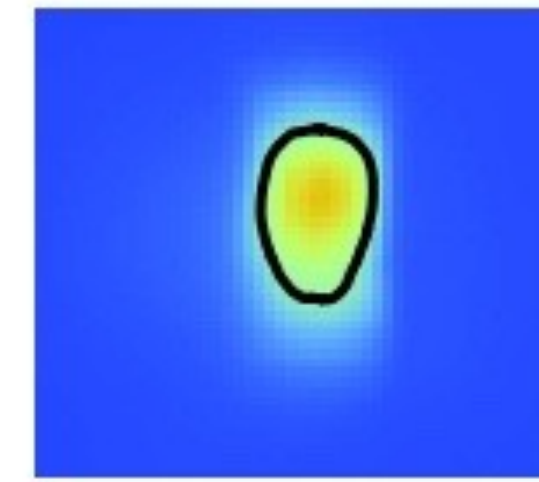
5.0 V, 4.7k Ω



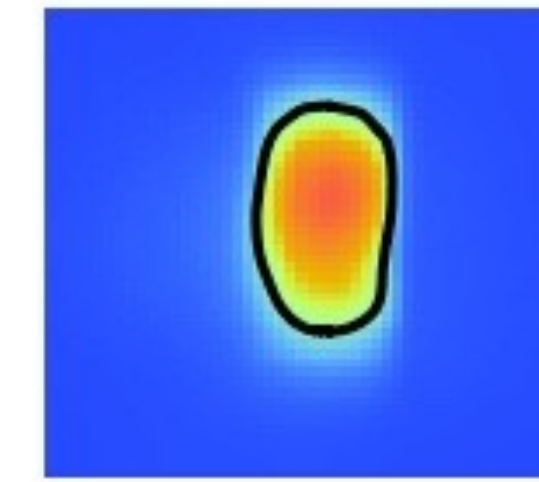
5.0 V, 10k Ω



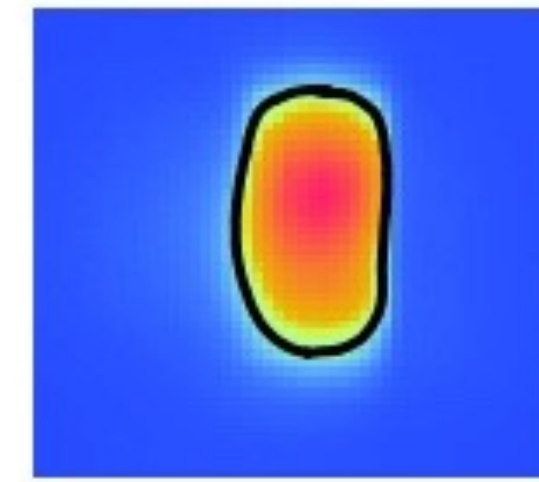
3.3 V, 1k Ω



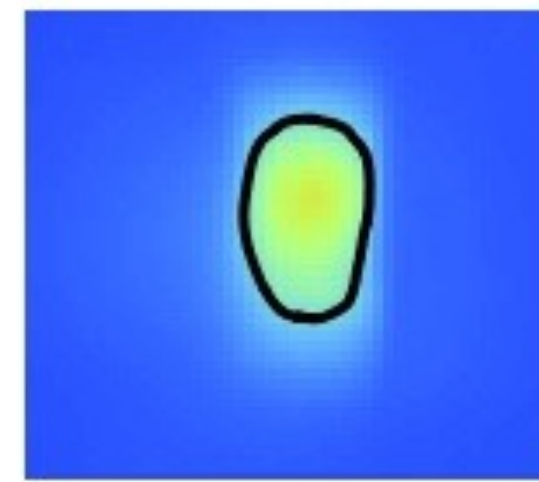
3.3 V, 2.2k Ω



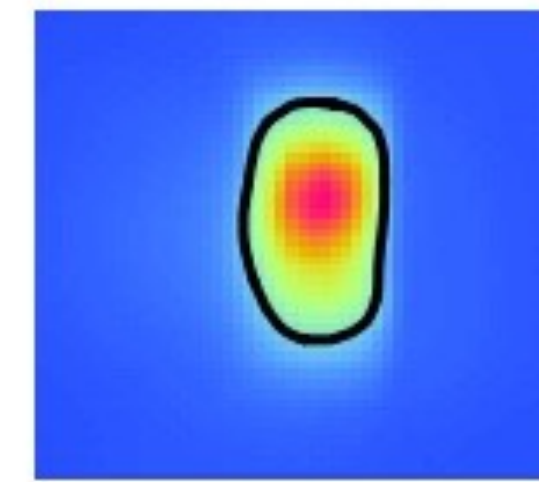
3.3 V, 4.7k Ω



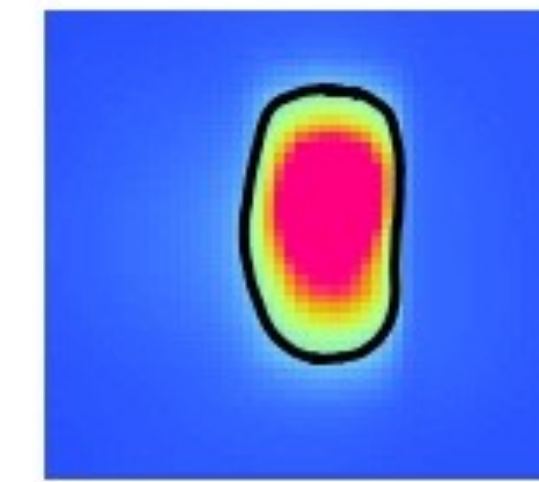
3.3 V, 10k Ω



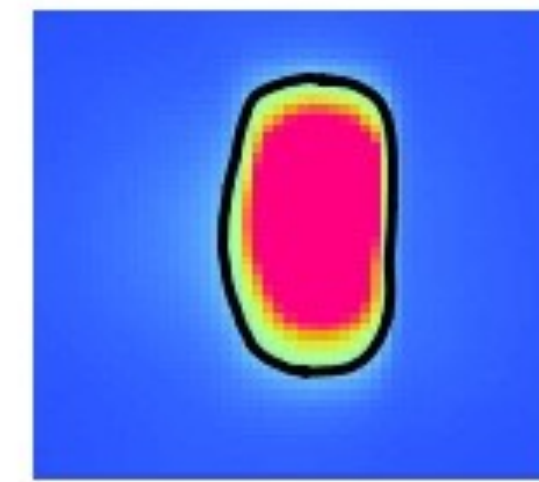
2.5 V, 1k Ω



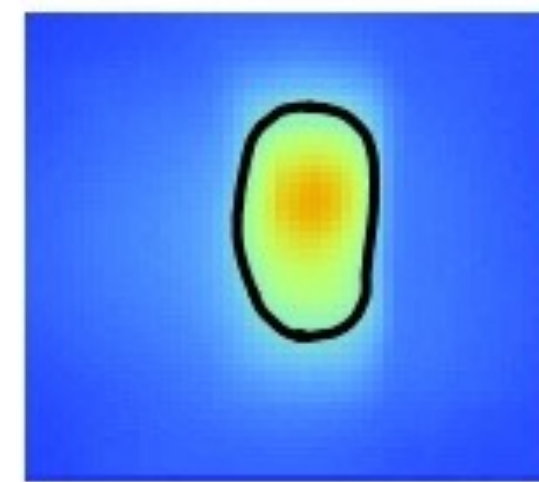
2.5 V, 2.2k Ω



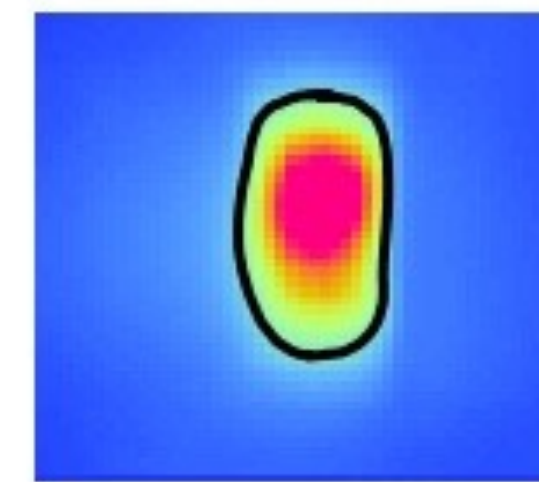
2.5 V, 4.7k Ω



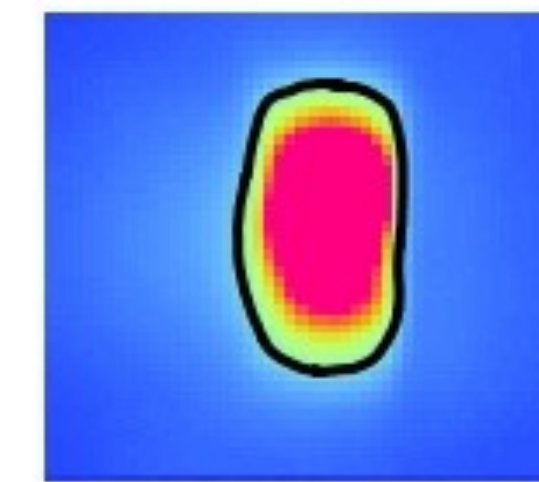
2.5 V, 10k Ω



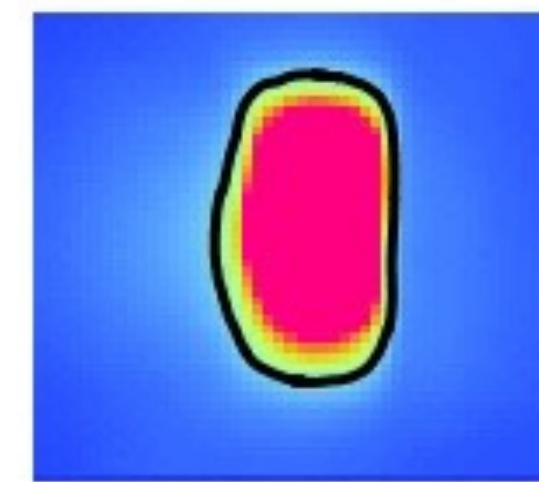
1.8 V, 1k Ω



1.8 V, 2.2k Ω

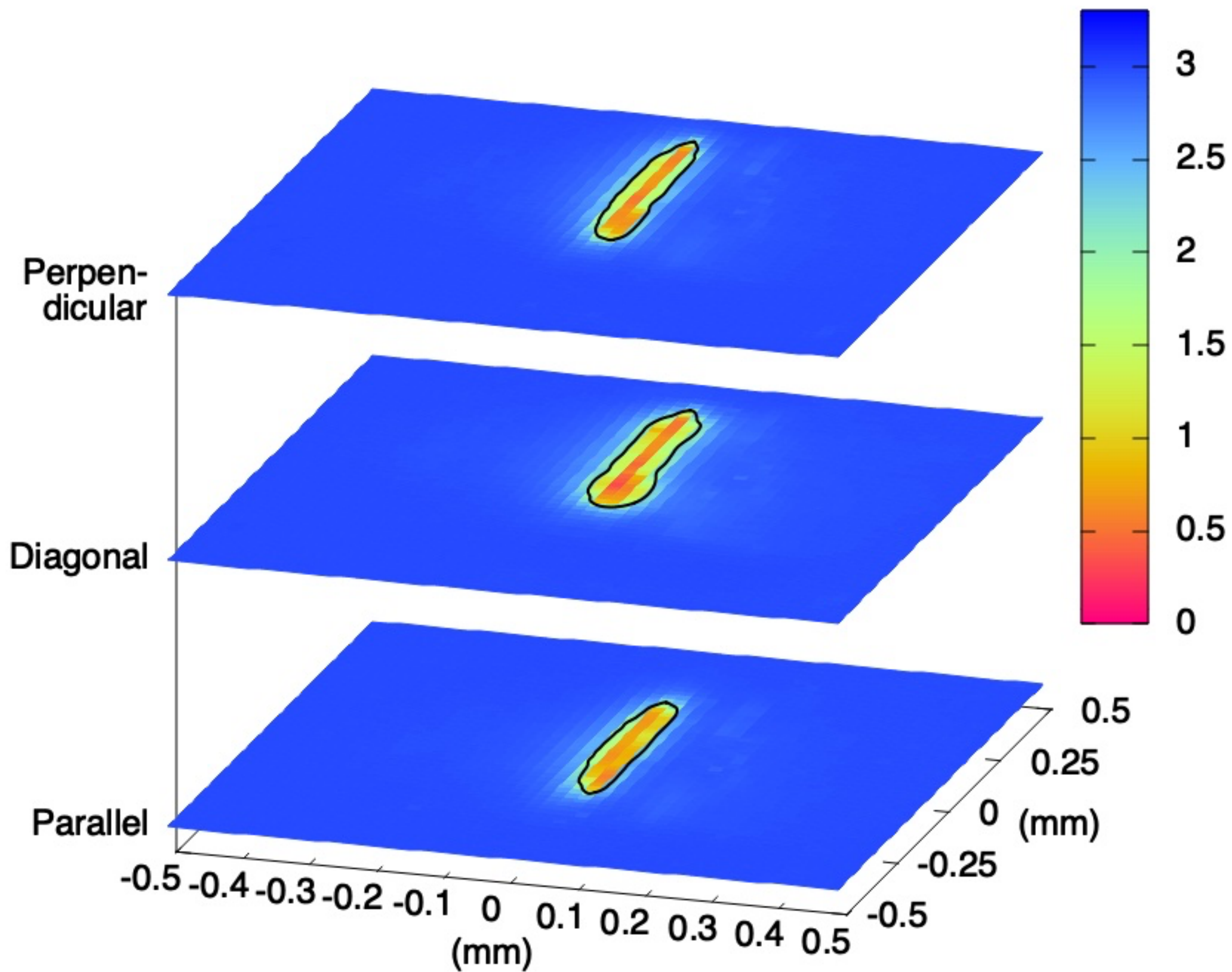


1.8 V, 4.7k Ω



1.8 V, 10k Ω

1N34A (Schottky silicon equivalent) DO-35 glass at 980 nm, 3.3 V, 10k Ω pull-up



Q&A